

8 7 6 5 4 3 2 1  
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F HSF Property: ROHS or Halogen-Free

ERIS  
MB

2012.05.03

24-April-2012		
DATE	CHANGE NO.	REV

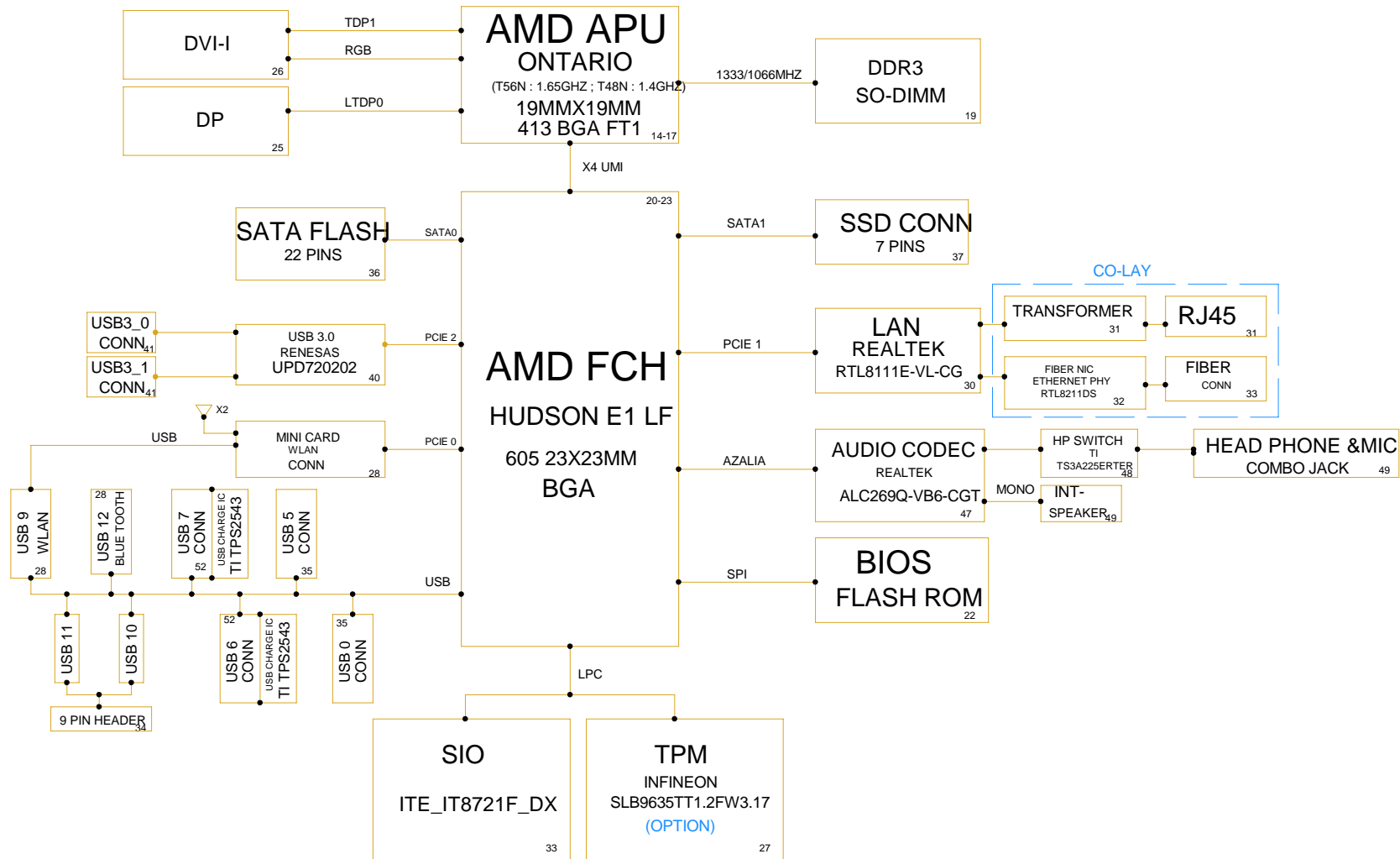
	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					title Eris COVER			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER:	SIZE C	CODE CS	DOC NUMBER 13110A25269-0-0	REV X01
FILE NAME								
PIN								

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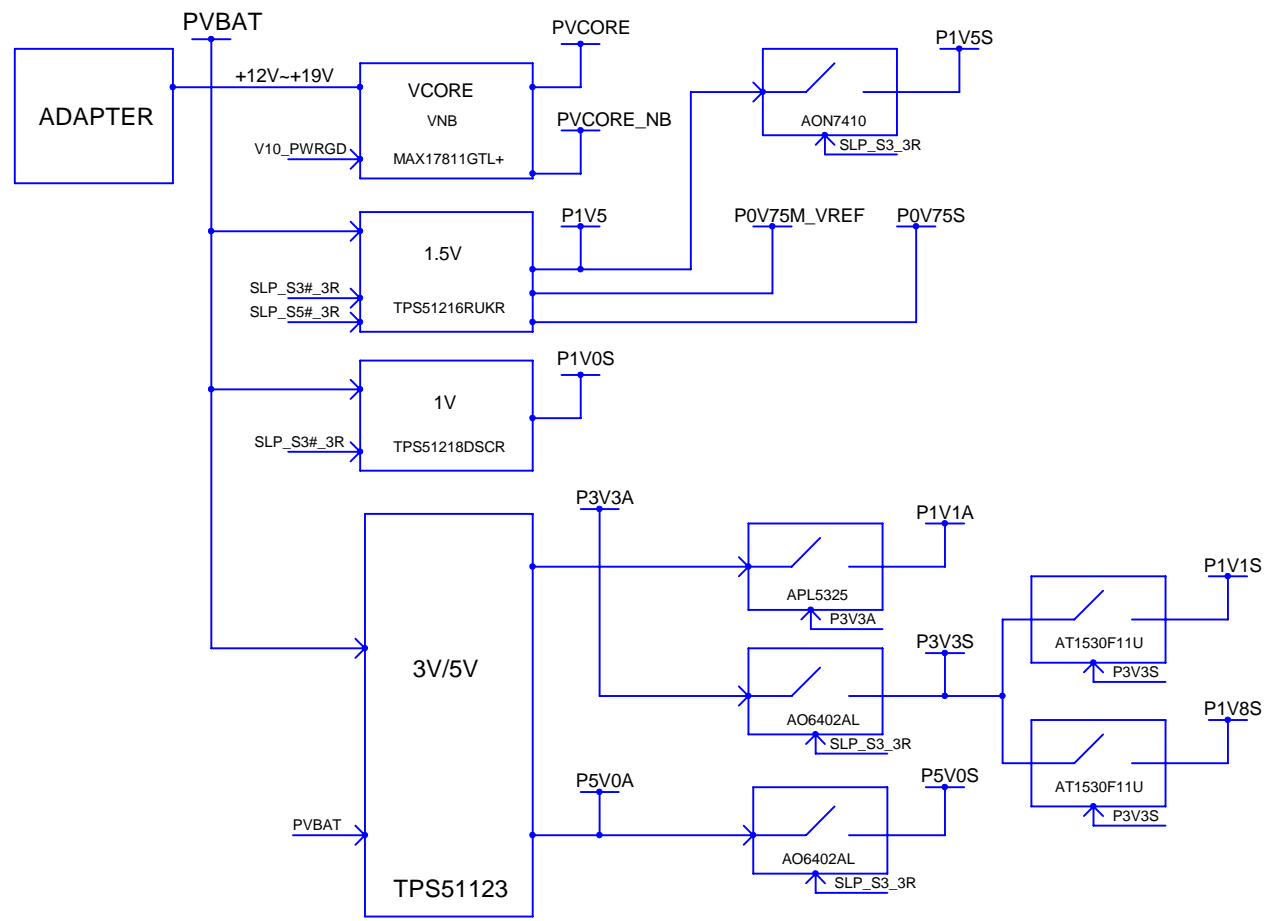
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7. POWER DC-IN	34. USB HEADER
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9. POWER +V3A/+V5A	36. SATA 22PIN CONN (FLASH)
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12. POWER VCORE/VCORE_NB1	39. GOOGLE DEBUG PORT & SW
13. POWER VCORE/VCORE_NB2	40. USB3.0 CONTROLLER
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24. STRAP OPTION / THERMAL / RTC	51. FRONT SIDE USB W/ CHARGING
25. DISPLAY PORT	52. FRONT SIDE USB CONN
26. DVI-I	53. POWER SW & LEDS
27. TPM	54. EMI/SCREW ON AUDIO DB

INVENTEC

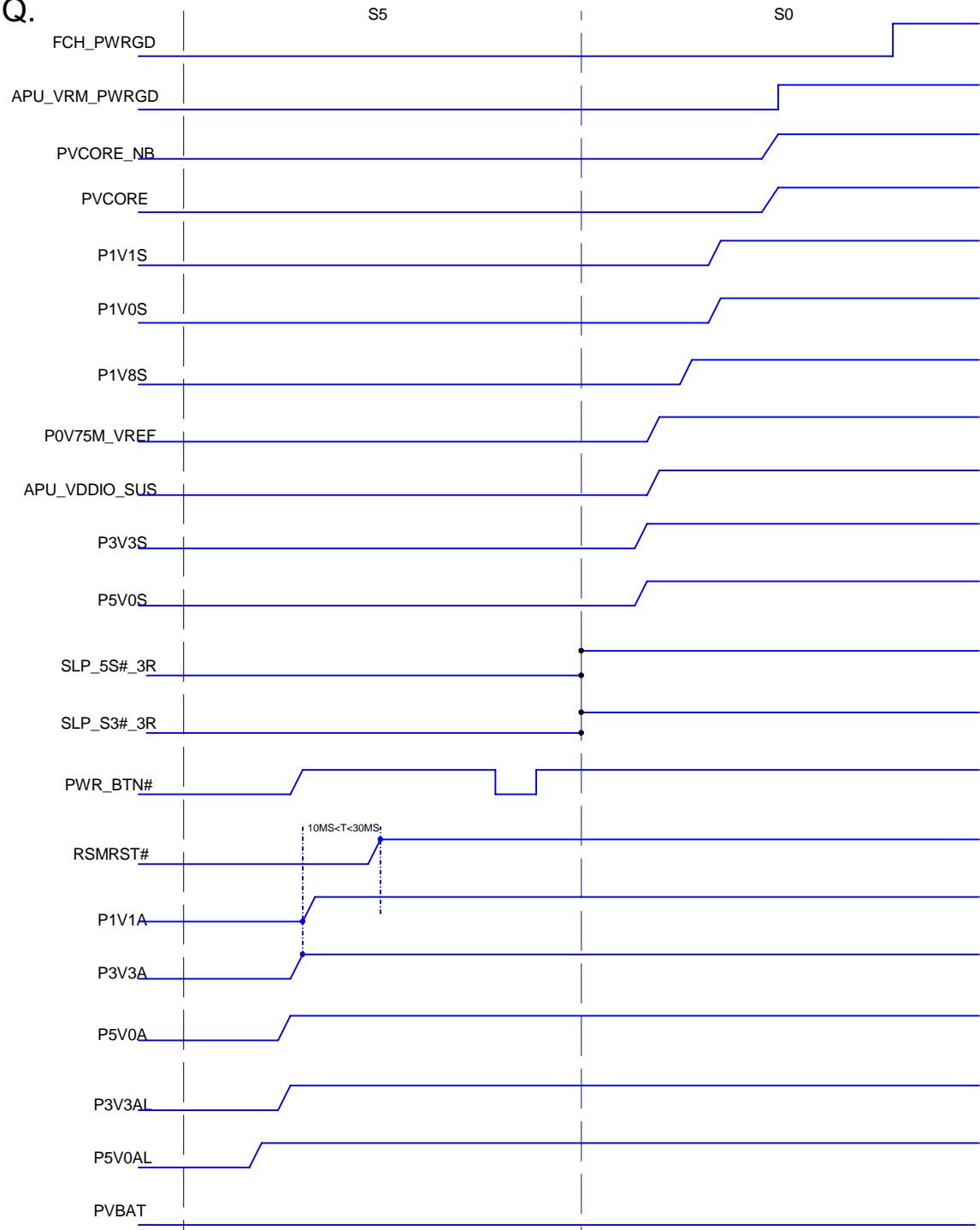
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SIZE	C	CODE	CS	DOC NUMBER	1310A25269-0-0	REV	201
SHEET		2		of		54	



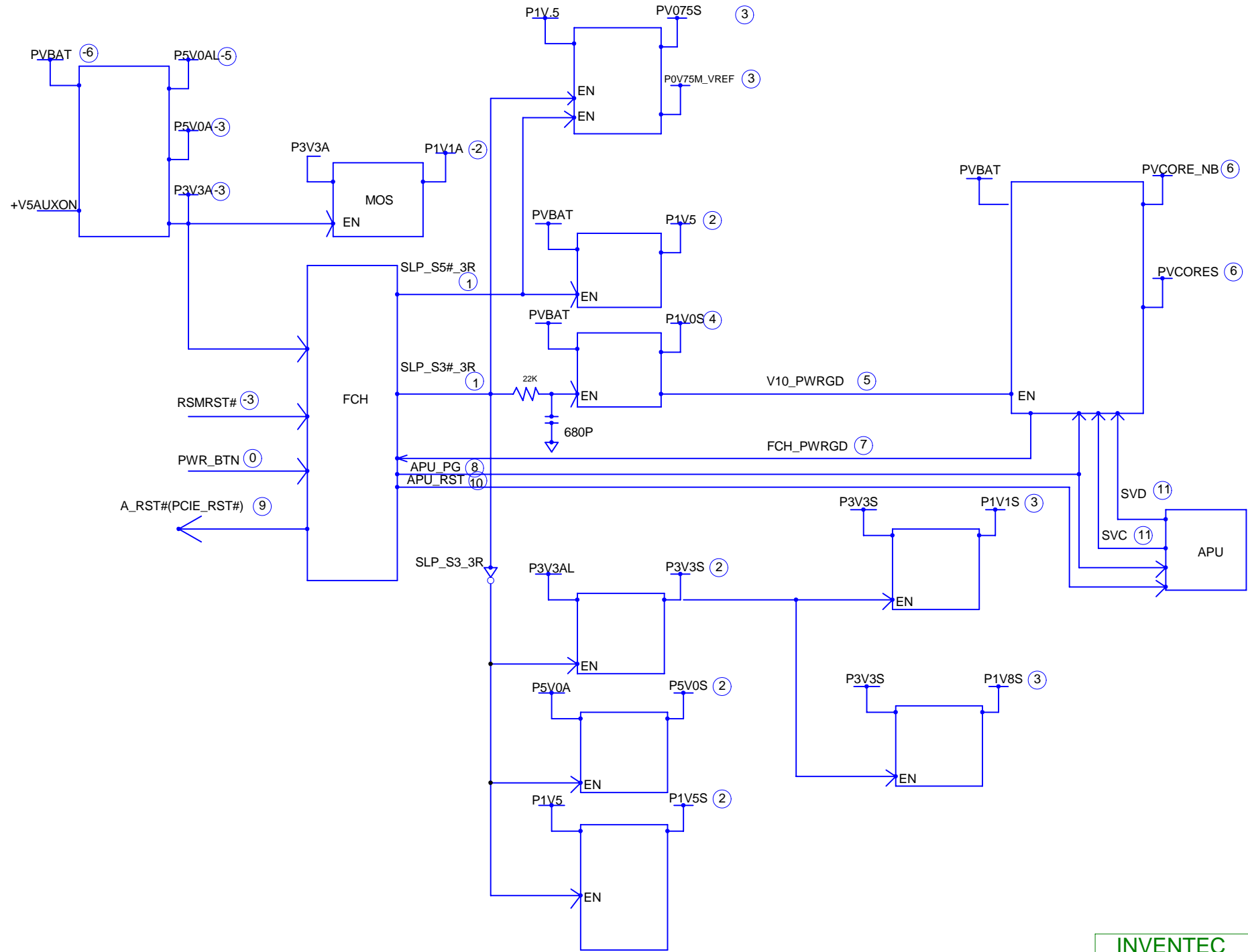
# POWER BLOCK DIAGRAM



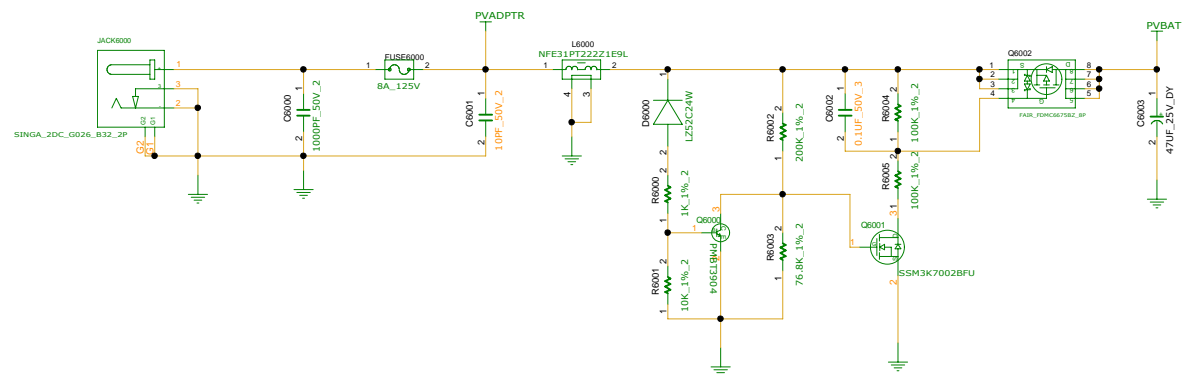
POWER ON SEQ.



POWER SEQUENCE



AC IN



INVENTEC

TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
SHEET 7 of 54			





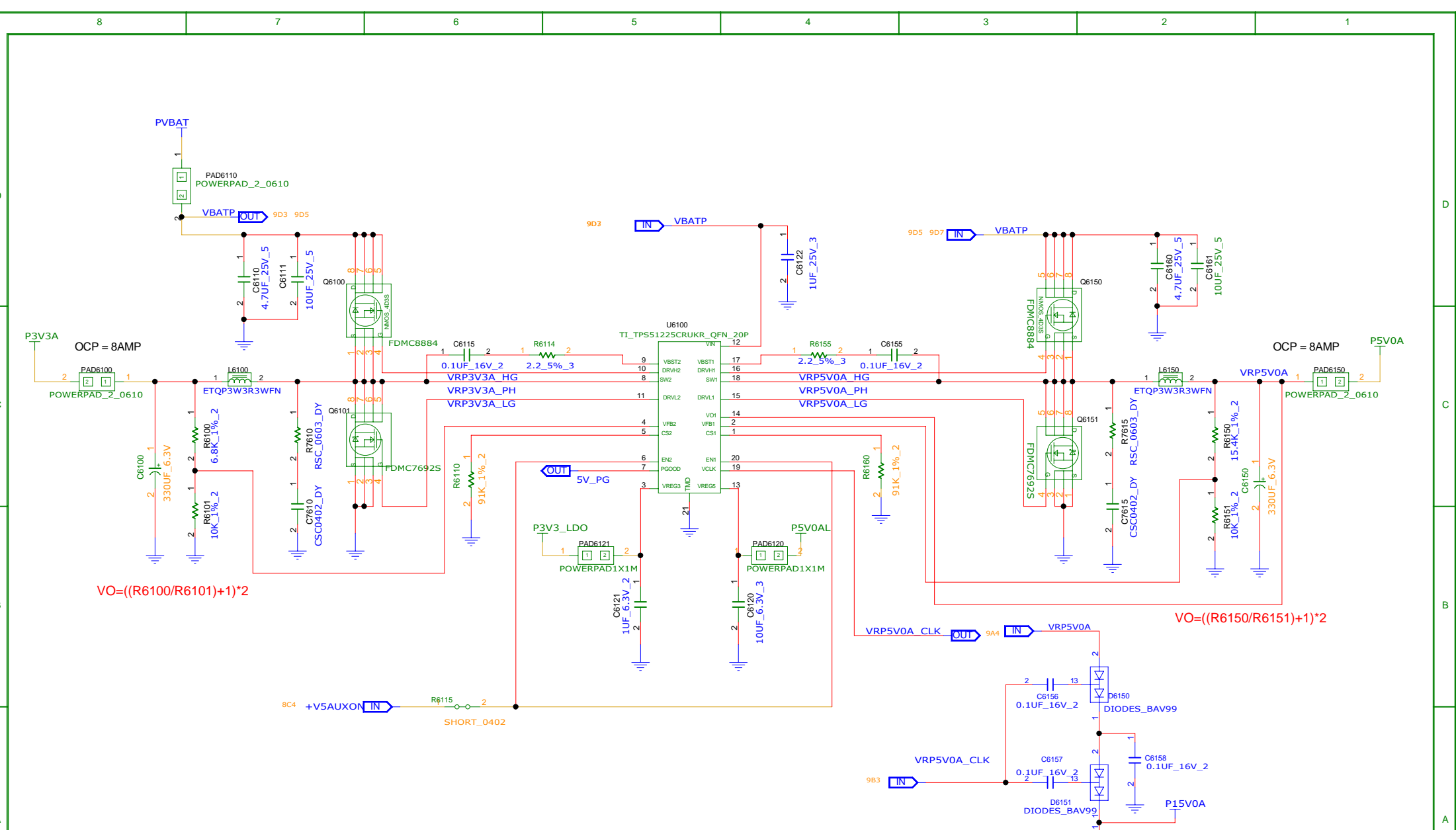


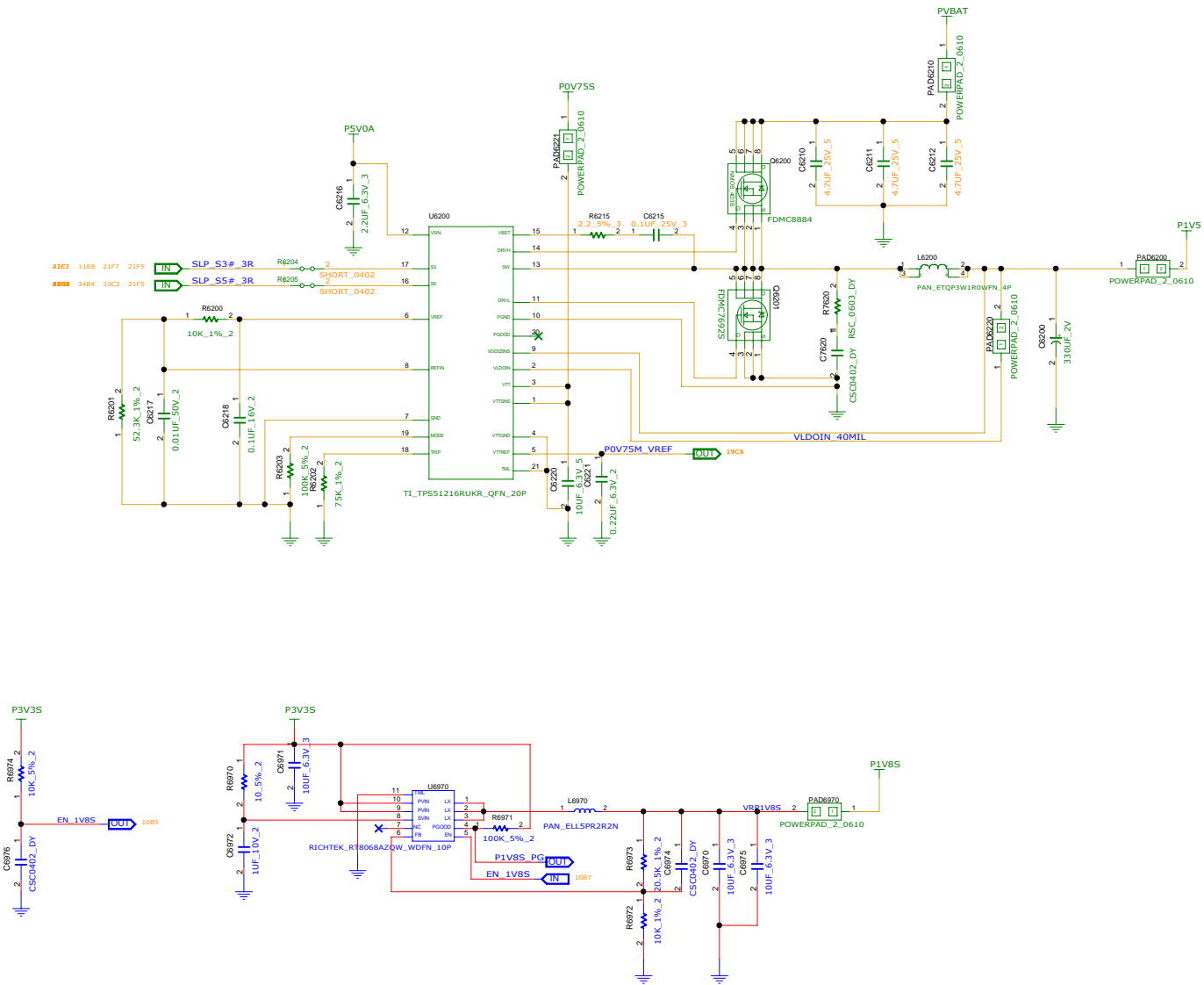
Table 2-3. Enabling State (51225C)

EN1	EN2	VREG5	VREG3	CH1 (5Vout)	CH2 (3.3Vout)	VCLK
OFF	OFF	ON	ON	OFF	OFF	OFF
ON	OFF	ON	ON	ON	OFF	ON
OFF	ON	ON	ON	OFF	ON	OFF
ON	ON	ON	ON	ON	ON	ON

INVENTEC

TITLE			
Eris Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A25269-0-0	X01

+V1.8S +V0.75S +V1.5



INVENTEC

Title		Eris	
Block		Diagram	
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A25269-0-0	X01
SHEET		10	of 54

A B C D E F

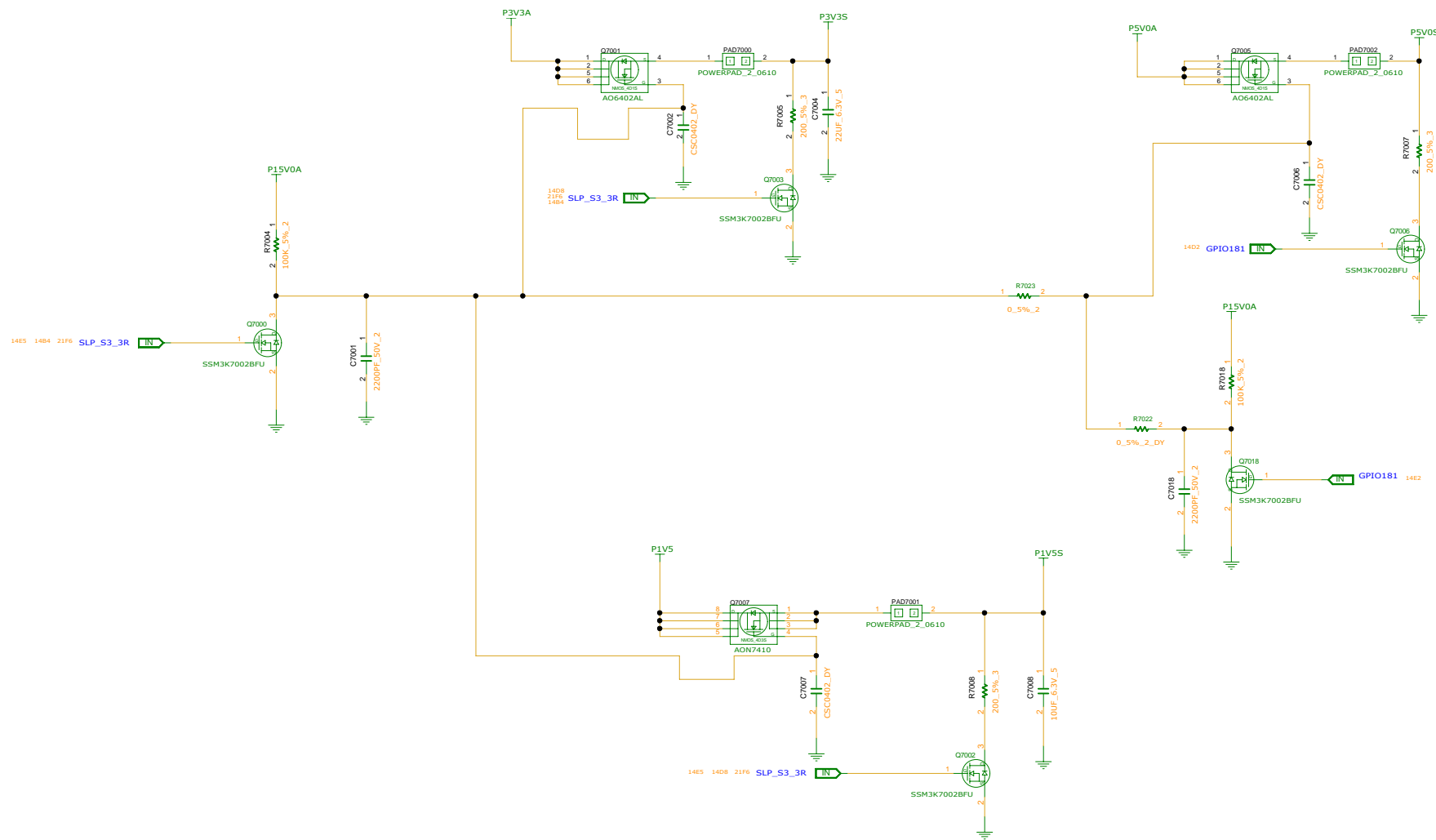


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SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
SHEET 11 of 54			





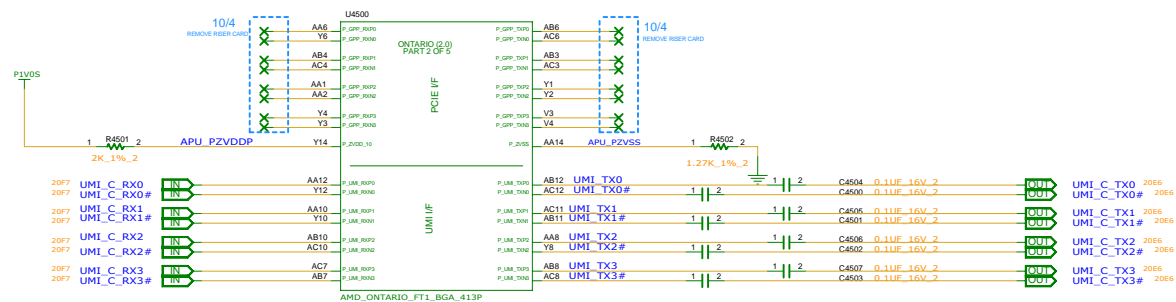
# +V5S +V3S +V1.5S +VAUDIO\_5S



INVENTEC

Title		Eris	
Block		Diagram	
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A25269-0-0	X01
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CHANGE by: Jeffery DATE: 24-ABR-2012

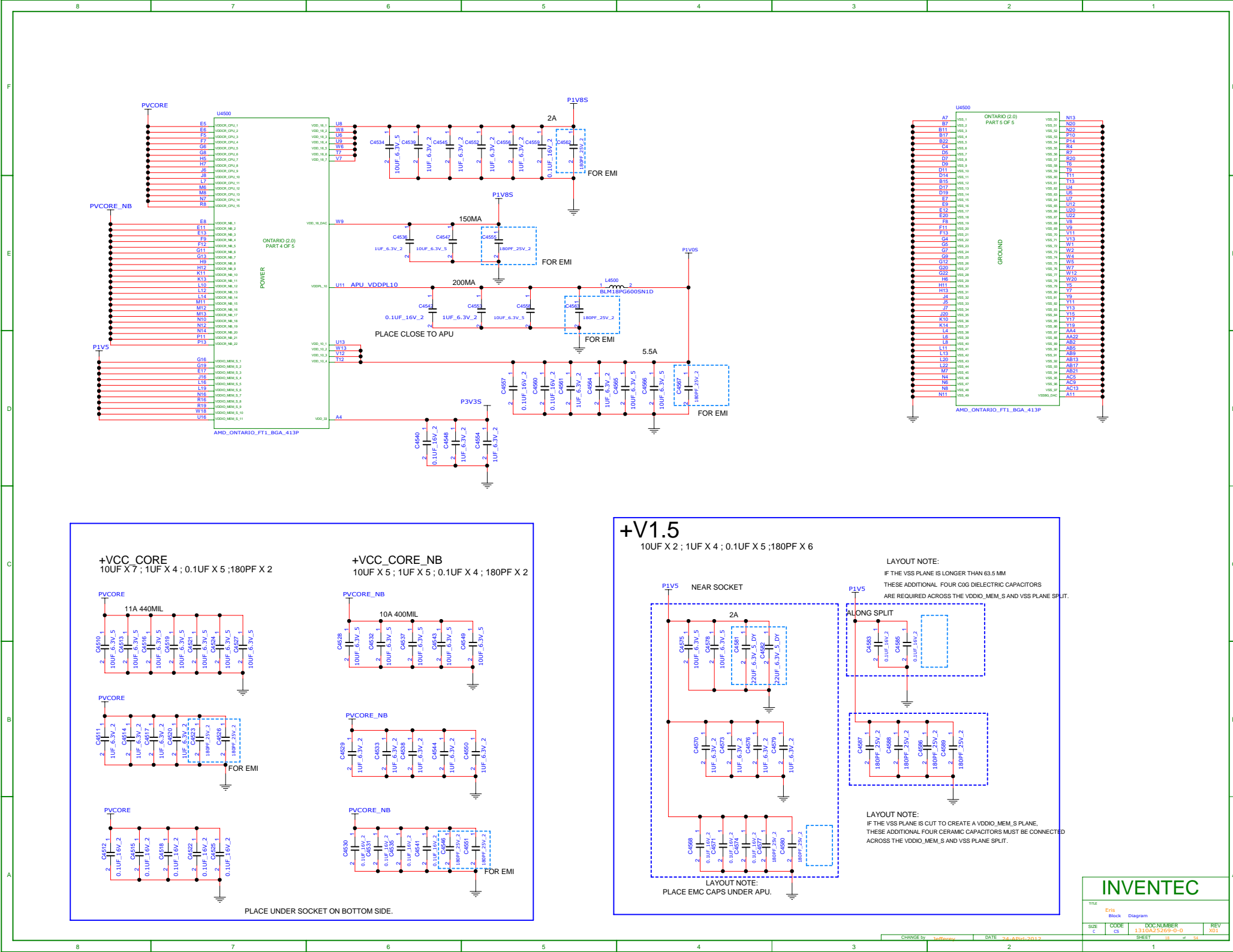


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Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
SHEET		15	of 54





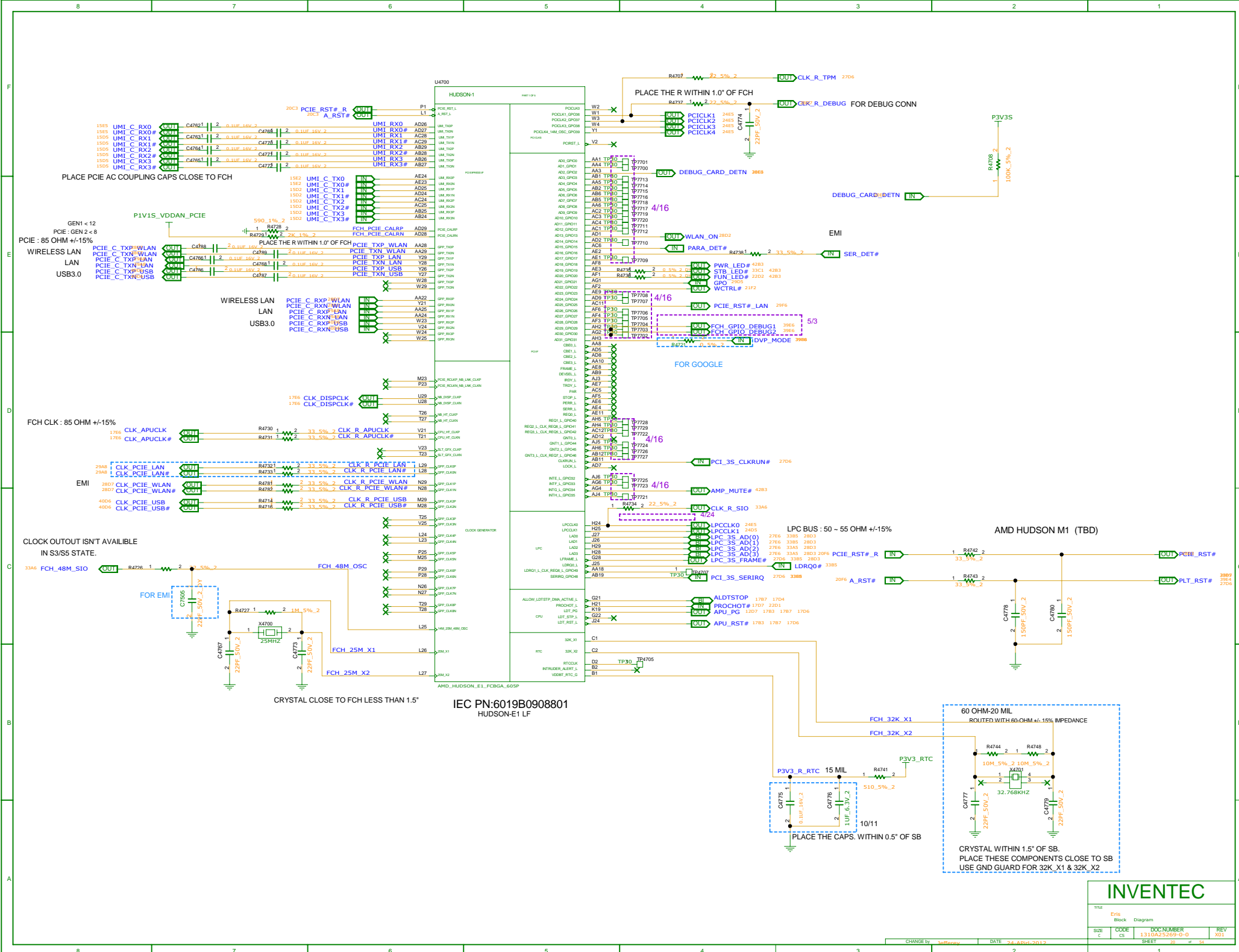


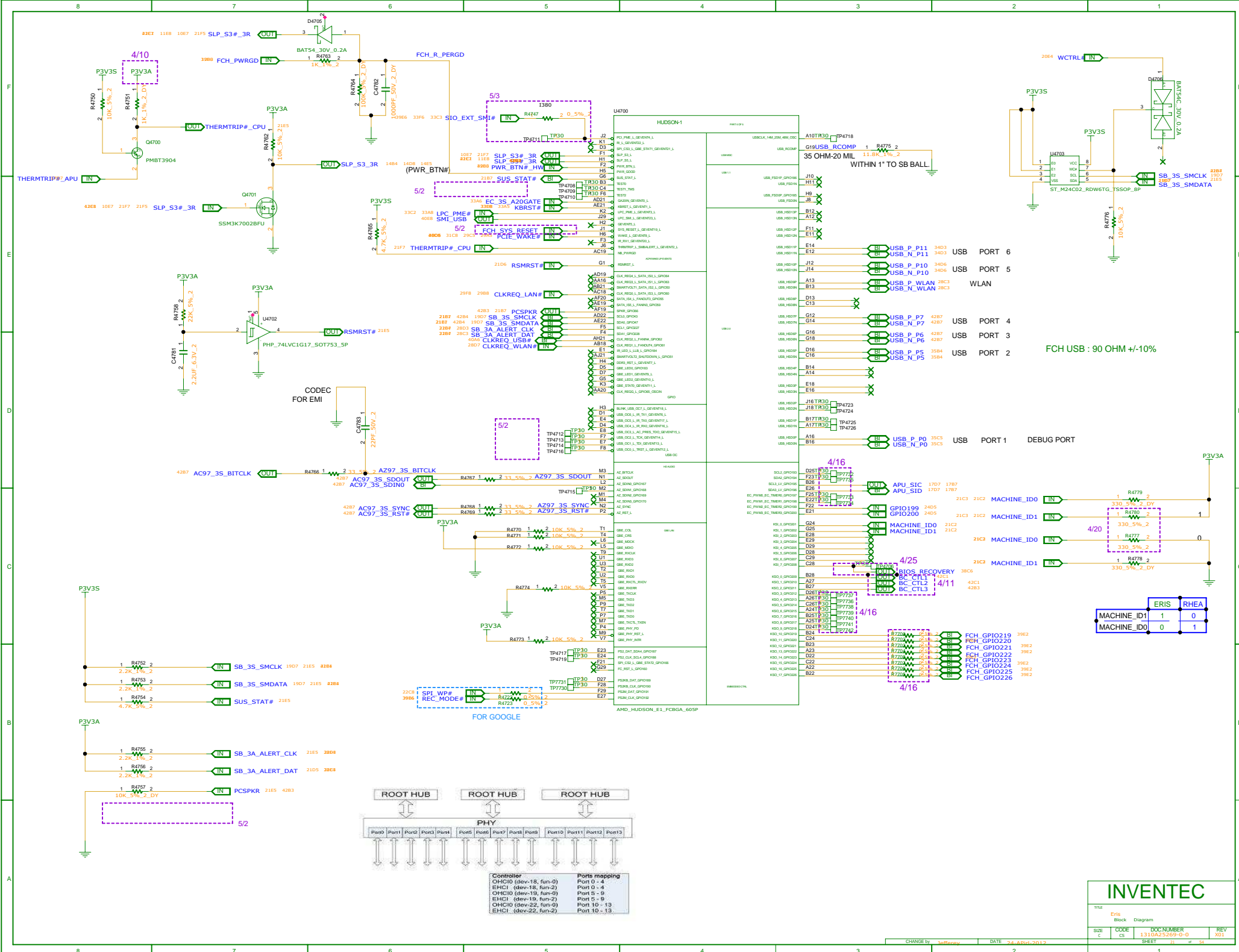


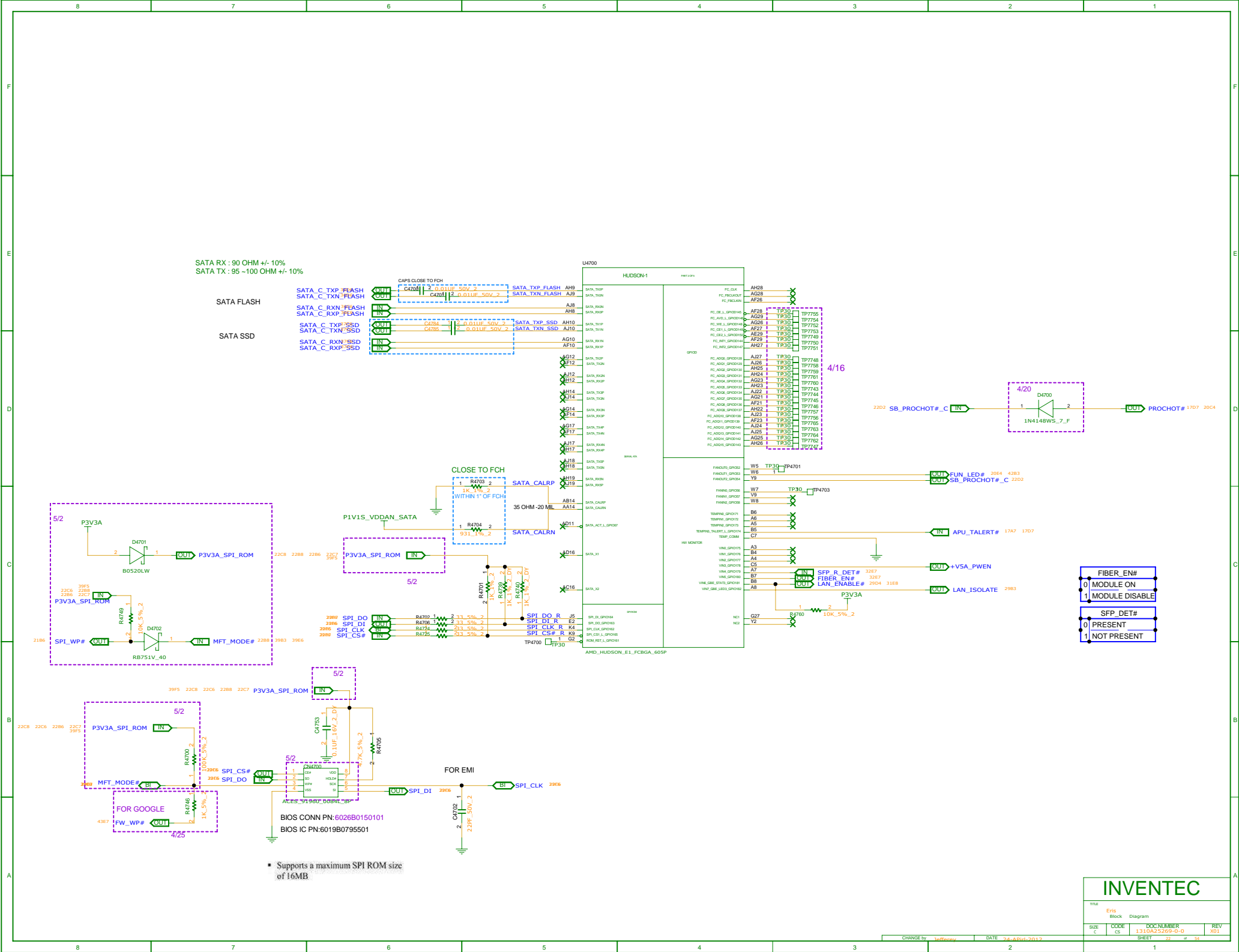
A B C D E F



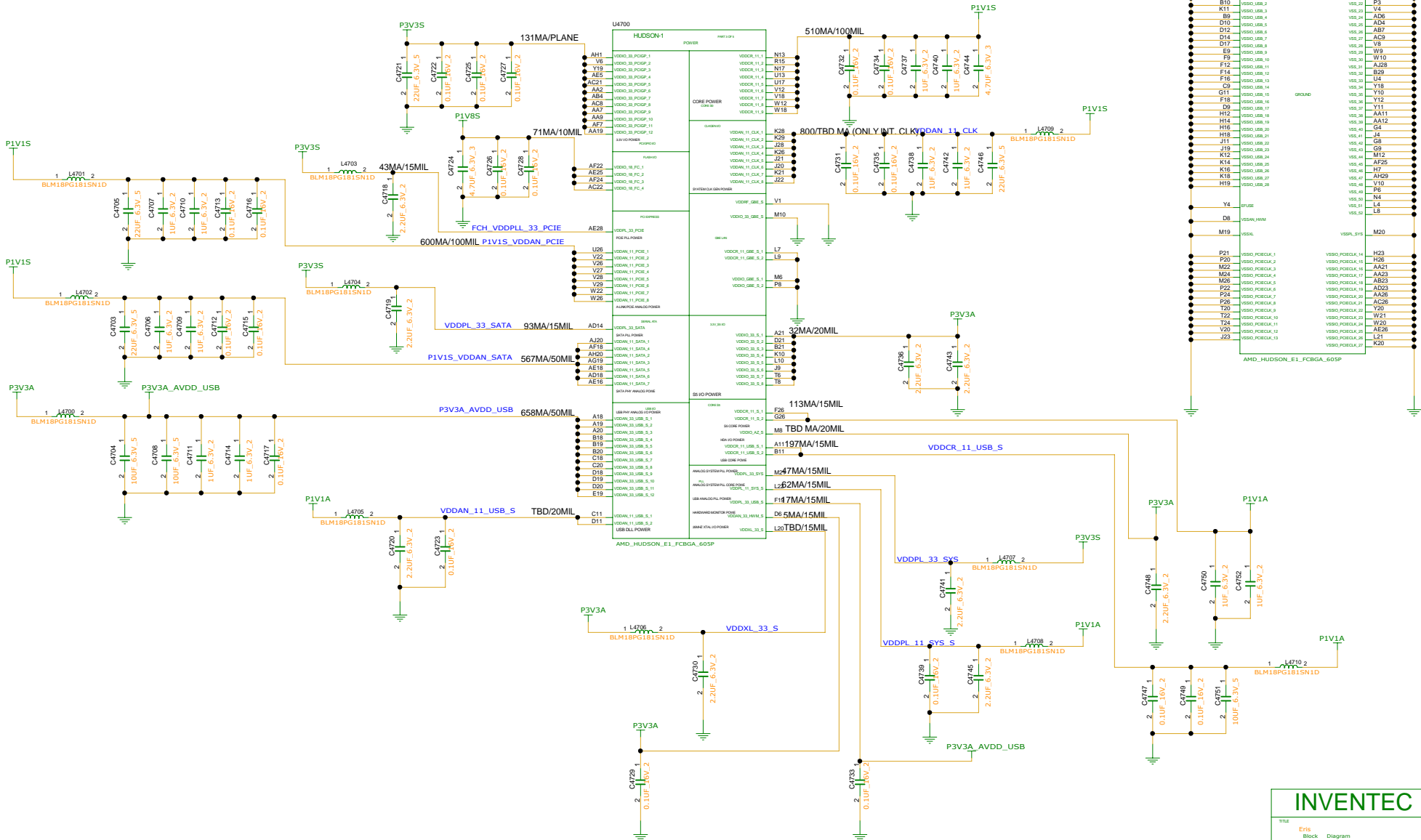
B





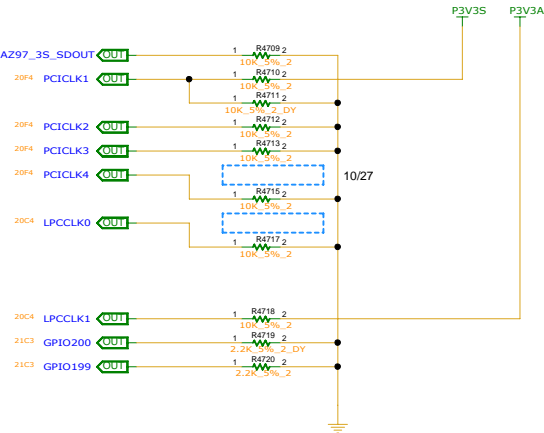


PLACE ALL THE DECOUPLING CAPS ON  
THIS SHEET CLOSE TO SB AS POSSIBLE

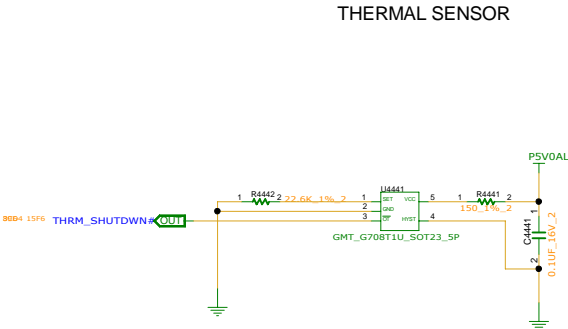
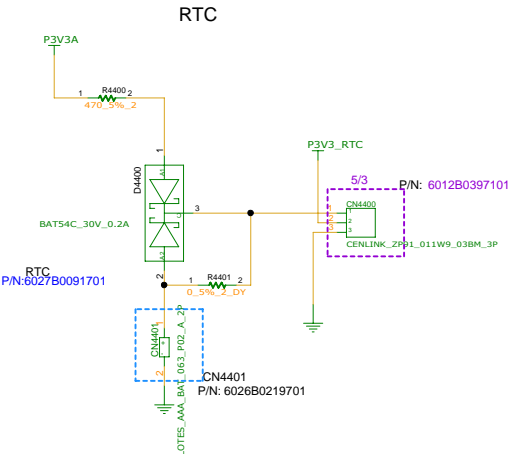


<b>INVENTEC</b>			
Title: Eris			
Block Diagram			
SIZE: C	CODE: 1310A25200-0	DOC NUMBER: 1310A25200-0	REV: 001
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STRAP OPTION / THERMAL / RTC



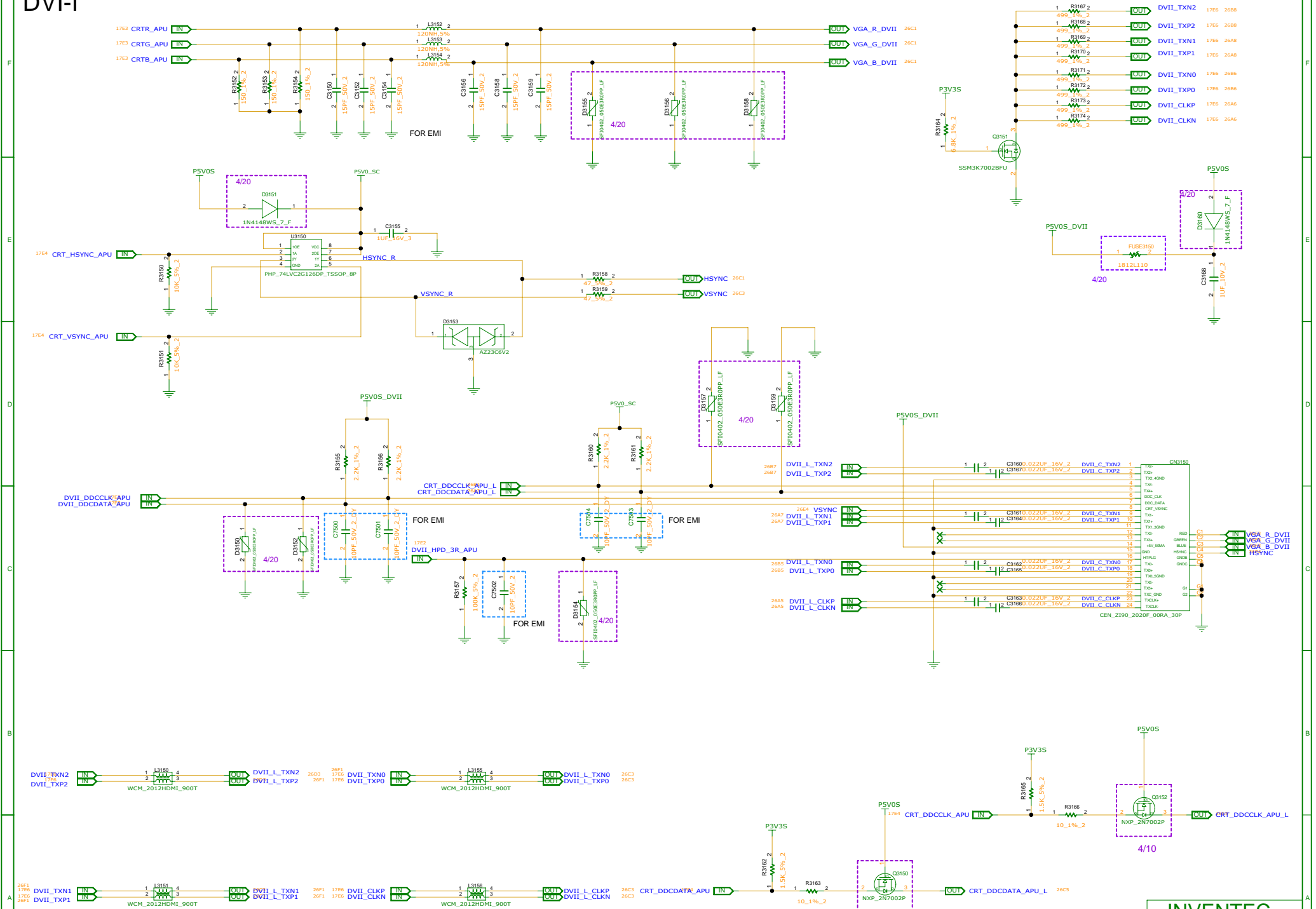
	AZ97_3S_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPCCLK0	LPCCLK1	GPIO200	GPIO199
								EC_PWM3	EC_PWM2
PULL HIGH	LOW POWER MODE (NOT SUPPORTED)	ALLOW PCIE GEN2 (DEFAULT)	WATCHINGDOG TIMER ENABLED (NOT SUPPORTED)	DEBUG STRAP ENABLED	NON-FUSION CLOCK MODE	INTEGRATED MICRO-CONTROLLER ENABLED (DEFAULT)	INTERNAL CLOCK GENERATOR ENABLED (DEFAULT)	H,H= RESERVED H,L= SPI ROM (DEFAULT)	
PULL LOW	PREFORMANCE MODE (DEFAULT)	FORCE PCIE GEN1	WATCHINGDOG TIMER DISABLED (DEFAULT)	DEBUG STRAP DISABLED (DEFAULT)	FUSION CLOCK MODE (DEFAULT)	INTEGRATED MICRO-CONTROLLER DISABLED	INTERNAL CLOCK GENERATOR DISABLED	L,H= LPC ROM L,L= RESERVED	





TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
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1			

## DVI-I



INVENTEC

TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
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A	B	C	D	E	F
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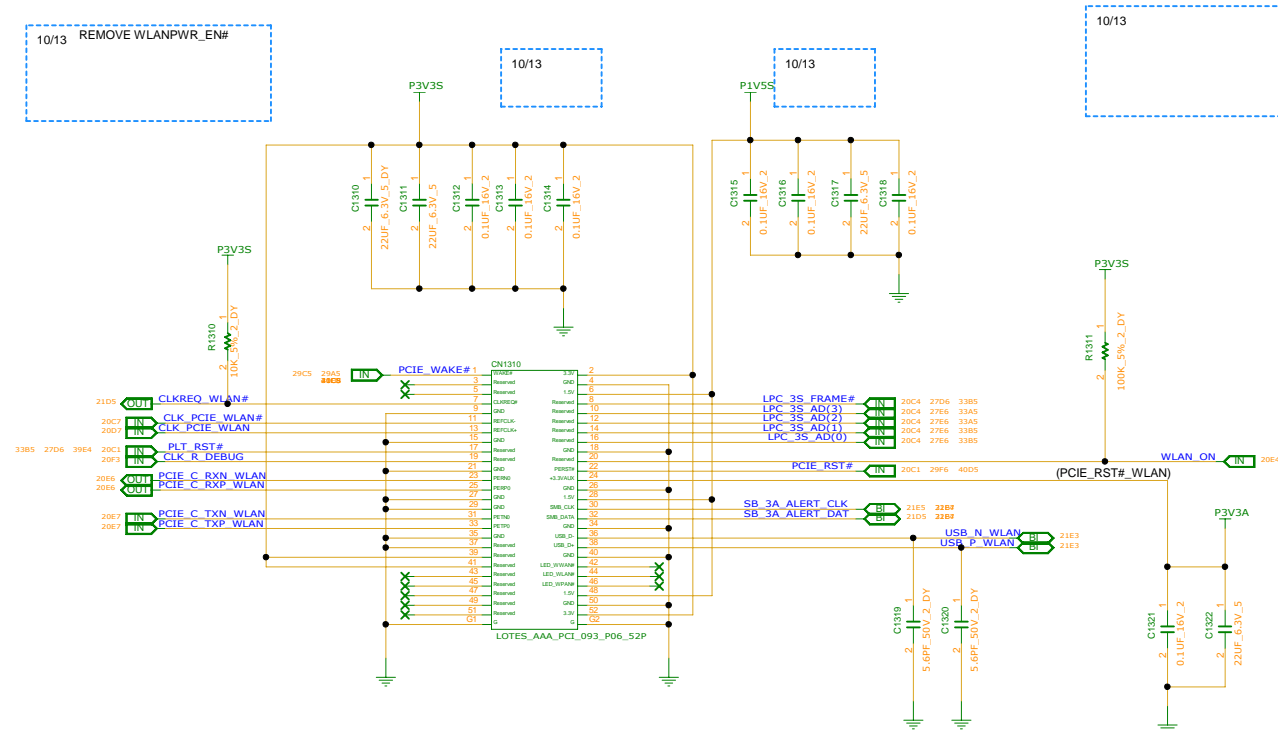


A

A

CHANGE by	jefferev	DATE	24-April-2017
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# MINICARD

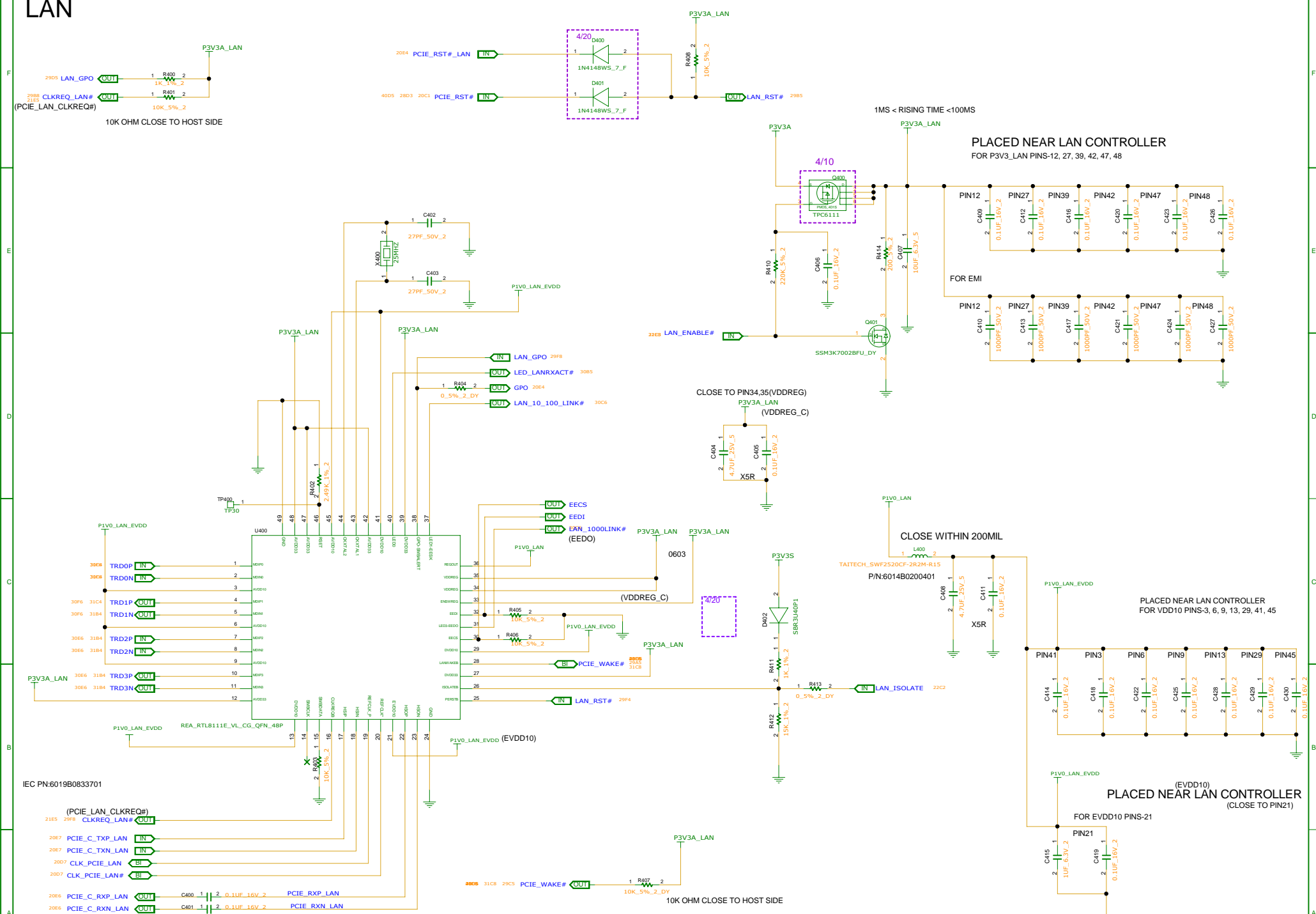


INVENTEC

Title		Eris	
Block		Diagram	
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A2520P-0-0	201
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CHANGE by: YefMansky DATE: 24-Apr-2012

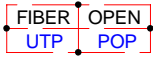
## LAN



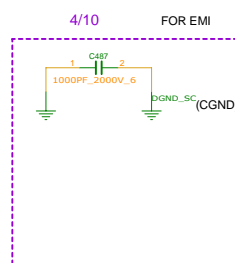
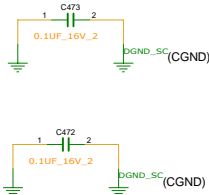
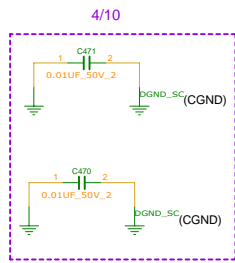
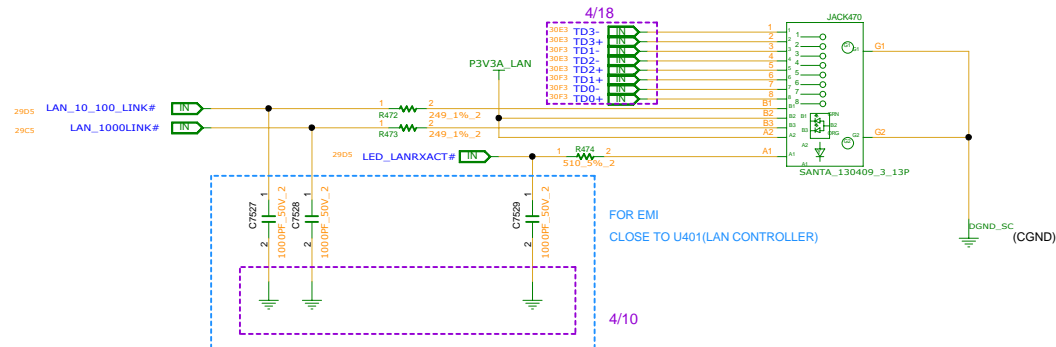
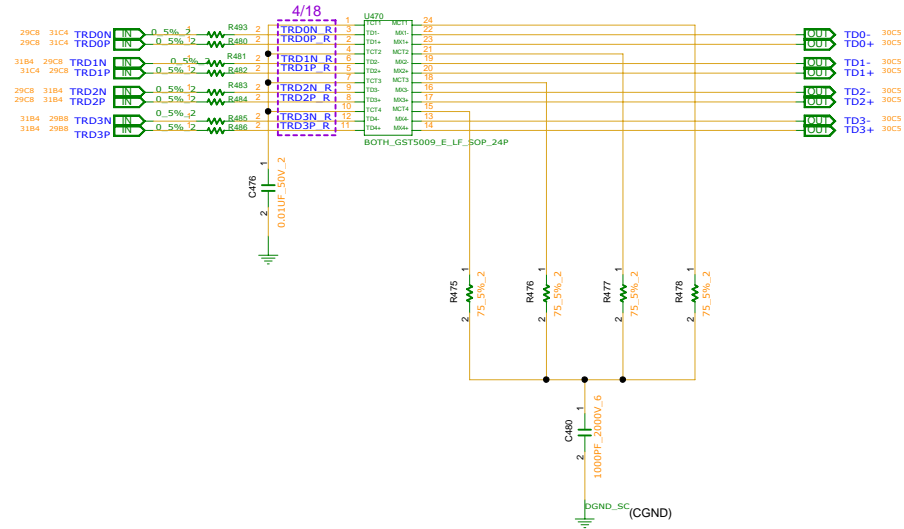
INVENTEC

TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
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# RJ45



P/N:6016B0014401



## INVENTEC

Title		Eris		Diagram	
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C	CS	1310A25269-0-0		X01	
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CHANGE by: *YoRover* DATE: 24-APR-2012

# LAN POWER

FIBER POP  
UTP OPEN

2908 LAN\_ENABLE# IN

SSM3K7002BFU\_DY

P3V3A

Q462

TPC6111

R453 220K\_5%\_2

C465 0.1UF\_10V\_2

R459 20K\_5%\_2

C461 10UF\_6.3V\_5

C451 22UF\_6.3V\_5

C450 0.1UF\_10V\_2

C444 0.1UF\_10V\_2

C443 0.1UF\_10V\_2

C442 0.1UF\_10V\_2

C441 0.1UF\_10V\_2

C437 0.1UF\_10V\_2

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C434 0.1UF\_10V\_2

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C423 0.1UF\_10V\_2

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C420 0.1UF\_10V\_2

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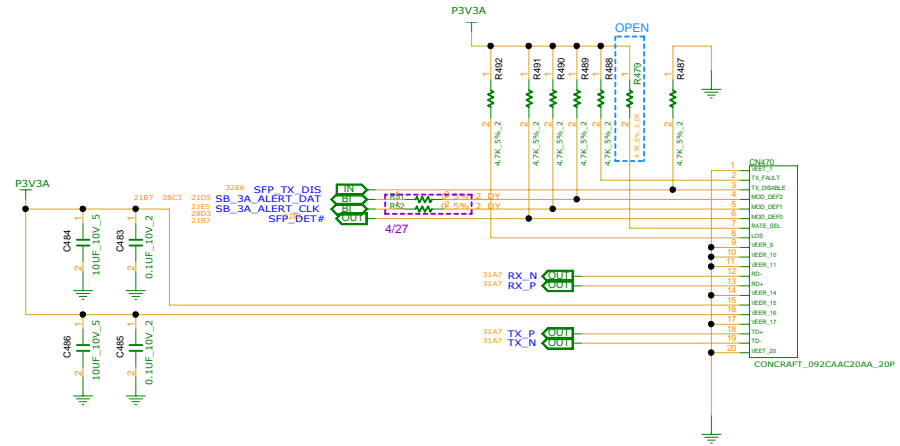
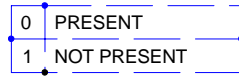
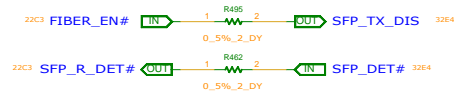
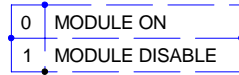
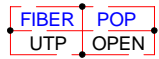
C236 0.1

# EXTERNAL POWER SOURCE

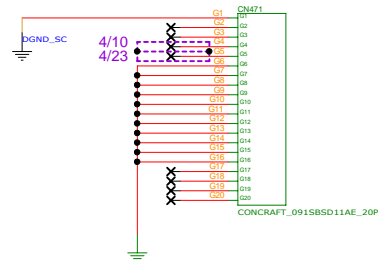
The diagram is a complex PCB schematic. At the top, there's a section for "FIBER MODE POWER PIN" showing a power supply network with components like C453, C452, and C451, and a note about trace width. Below this, a "FIBER MODE: POP UTP MODE: OPEN" section shows signal traces for TX\_P, TX\_N, RX\_P, and RX\_N, with components like C450, C451, C452, C453, and C454. The central part of the diagram features a large component U401, which is a multi-pin connector or IC, with numerous pins labeled. To its right is component U400, which is a multi-pin connector or IC, also with many pins labeled. Below U401 is component U402, which is a multi-pin connector or IC, with pins labeled. The diagram includes various other components like resistors (R455, R456, R457, R458, R459, R460, R461, R462, R463, R464, R465, R466, R467, R468, R469, R470, R471, R472, R473, R474, R475, R476, R477, R478, R479, R480, R481, R482, R483, R484, R485, R486, R487, R488, R489, R490, R491, R492, R493, R494, R495, R496, R497, R498, R499, R500, R501, R502, R503, R504, R505, R506, R507, R508, R509, R510, R511, R512, R513, R514, R515, R516, R517, R518, R519, R520, R521, R522, R523, R524, R525, R526, R527, R528, R529, R530, R531, R532, R533, R534, R535, R536, R537, R538, R539, R540, R541, R542, R543, R544, R545, R546, R547, R548, R549, R550, R551, R552, R553, R554, R555, R556, R557, R558, R559, R560, R561, R562, R563, R564, R565, R566, R567, R568, R569, R570, R571, R572, R573, R574, R575, R576, R577, R578, R579, R580, R581, R582, R583, R584, R585, R586, R587, R588, R589, R590, R591, R592, R593, R594, R595, R596, R597, R598, R599, R600, R601, R602, R603, R604, R605, R606, R607, R608, R609, R610, R611, R612, R613, R614, R615, R616, R617, R618, R619, R620, R621, R622, R623, R624, R625, R626, R627, R628, R629, R630, R631, R632, R633, R634, R635, R636, R637, R638, R639, R640, R641, R642, R643, R644, R645, R646, R647, R648, R649, R650, R651, R652, R653, R654, R655, R656, R657, R658, R659, R660, R661, R662, R663, R664, R665, R666, R667, R668, R669, R670, R671, R672, R673, R674, R675, R676, R677, R678, R679, R680, R681, R682, R683, R684, R685, R686, R687, R688, R689, R690, R691, R692, R693, R694, R695, R696, R697, R698, R699, R700, R701, R702, R703, R704, R705, R706, R707, R708, R709, R710, R711, R712, R713, R714, R715, R716, R717, R718, R719, R720, R721, R722, R723, R724, R725, R726, R727, R728, R729, R730, R731, R732, R733, R734, R735, R736, R737, R738, R739, R740, R741, R742, R743, R744, R745, R746, R747, R748, R749, R750, R751, R752, R753, R754, R755, R756, R757, R758, R759, R760, R761, R762, R763, R764, R765, R766, R767, R768, R769, R770, R771, R772, R773, R774, R775, R776, R777, R778, R779, R780, R781, R782, R783, R784, R785, R786, R787, R788, R789, R790, R791, R792, R793, R794, R795, R796, R797, R798, R799, R800, R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R837, R838, R839, R840, R841, R842, R843, R844, R845, R846, R847, R848, R849, R850, R851, R852, R853, R854, R855, R856, R857, R858, R859, R860, R861, R862, R863, R864, R865, R866, R867, R868, R869, R870, R871, R872, R873, R874, R875, R876, R877, R878, R879, R880, R881, R882, R883, R884, R885, R886, R887, R888, R889, R890, R891, R892, R893, R894, R895, R896, R897, R898, R899, R900, R901, R902, R903, R904, R905, R906, R907, R908, R909, R910, R911, R912, R913, R914, R915, R916, R917, R918, R919, R920, R921, R922, R923, R924, R925, R926, R927, R928, R929, R930, R931, R932, R933, R934, R935, R936, R937, R938, R939, R940, R941, R942, R943, R944, R945, R946, R947, R948, R949, R950, R951, R952, R953, R954, R955, R956, R957, R958, R959, R960, R961, R962, R963, R964, R965, R966, R967, R968, R969, R970, R971, R972, R973, R974, R975, R976, R977, R978, R979, R980, R981, R982, R983, R984, R985, R986, R987, R988, R989, R990, R991, R992, R993, R994, R995, R996, R997, R998, R999, R1000, R1001, R1002, R1003, R1004, R1005, R1006, R1007, R1008, R1009, R1010, R1011, R1012, R1013, R1014, R1015, R1016, R1017, R1018, R1019, R1020, R1021, R1022, R1023, R1024, R1025, R1026, R1027, R1028, R1029, R1030, R1031, R1032, R1033, R1034, R1035, R1036, R1037, R1038, R1039, R1040, R1041, R1042, R1043, R1044, R1045, R1046, R1047, R1048, R1049, R1050, R1051, R1052, R1053, R1054, R1055, R1056, R1057, R1058, R1059, R1060, R1061, R1062, R1063, R1064, R1065, R1066, R1067, R1068, R1069, R1070, R1071, R1072, R1073, R1074, R1075, R1076, R1077, R1078, R1079, R1080, R1081, R1082, R1083, R1084, R1085, R1086, R1087, R1088, R1089, R1090, R1091, R1092, R1093, R1094, R1095, R1096, R1097, R1098, R1099, R1100, R1101, R1102, R1103, R1104, R1105, R1106, R1107, R1108, R1109, R1110, R1111, R1112, R1113, R1114, R1115, R1116, R1117, R1118, R1119, R1120, R1121, R1122, R1123, R1124, R1125, R1126, R1127, R1128, R1129, R1130, R1131, R1132, R1133, R1134, R1135, R1136, R1137, R1138, R1139, R1140, R1141, R1142, R1143, R1144, R1145, R1146, R1147, R1148, R1149, R1150, R1151, R1152, R1153, R1154, R1155, R1156, R1157, R1158, R1159, R1160, R1161, R1162, R1163, R1164, R1165, R1166, R1167, R1168, R1169, R1170, R1171, R1172, R1173, R1174, R1175, R1176, R1177, R

TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
SHEET 31 of 54			

# FIBER CONN



# HOUSING



INVENTEC

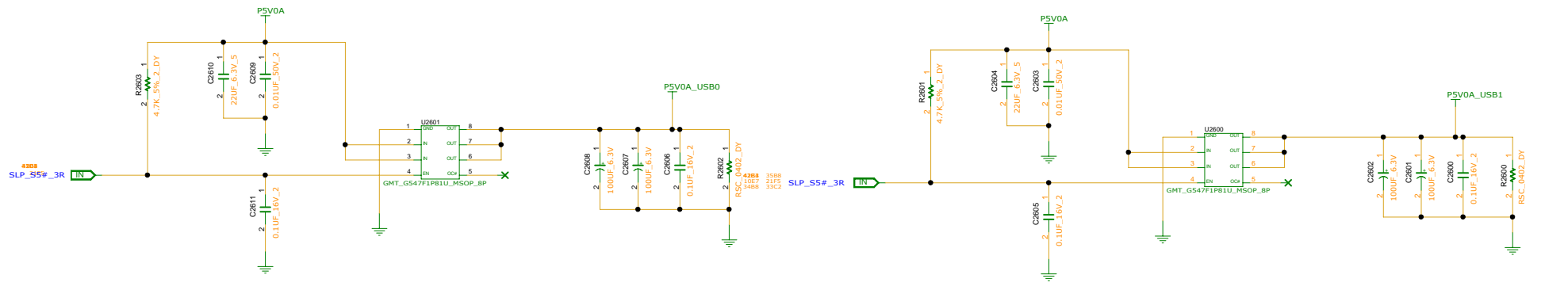
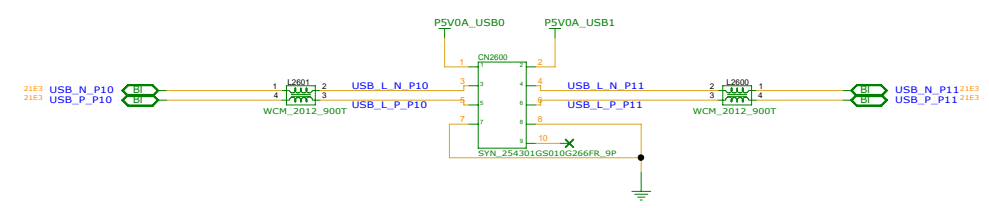
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SHEET		32	of	64	REV 201

CHANGE by: *YuRemy* DATE: 24-APR-2012



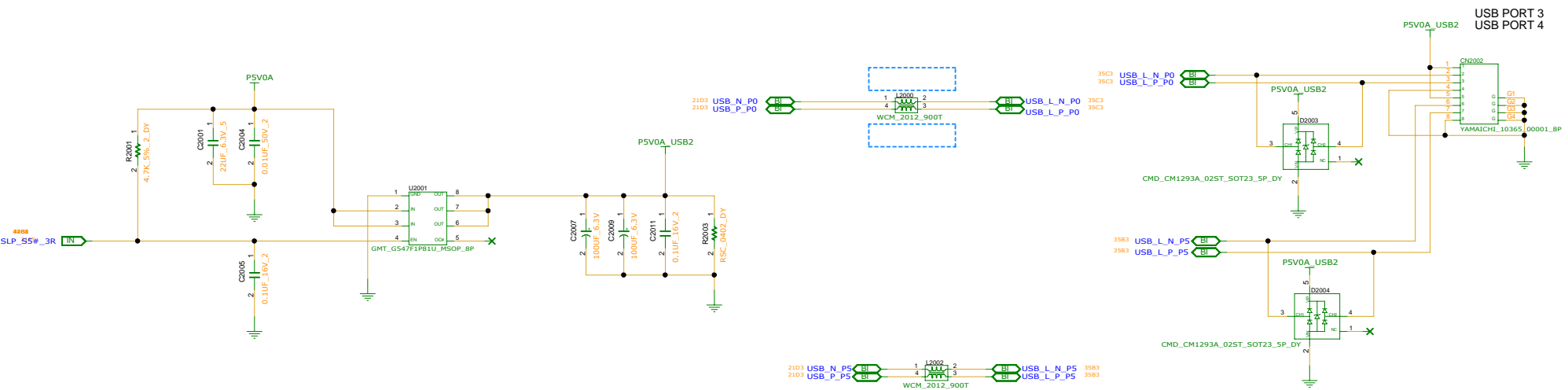
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# USB HEADER



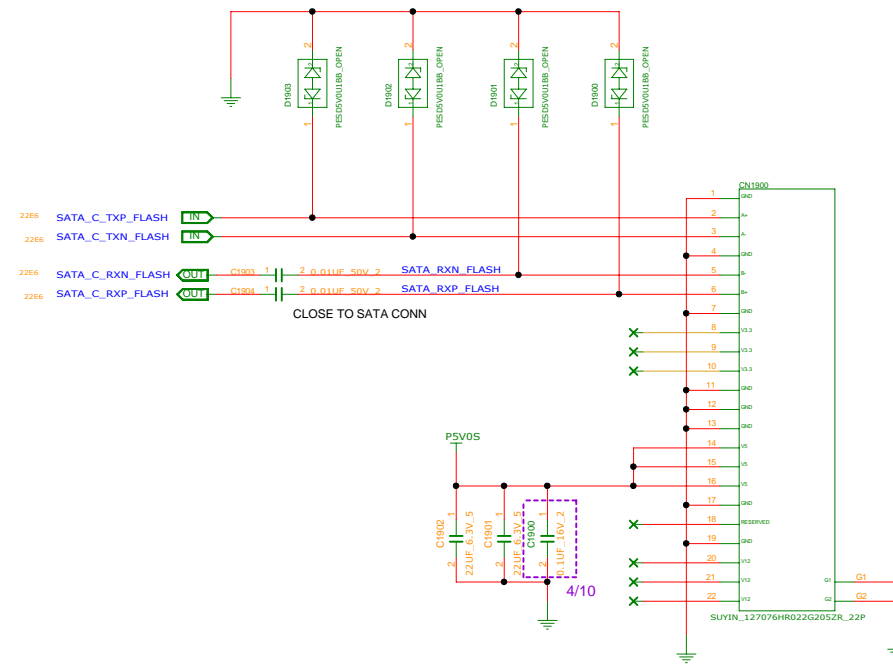
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Title			
Eris			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A02526P-0-0	X01
SHEET		34	of 64

# REAR SIDE USB PORT



INVENTEC			
Title Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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SHEET		35	of 54

# SATA 22PIN CONN (FOR FLASH MODULE)

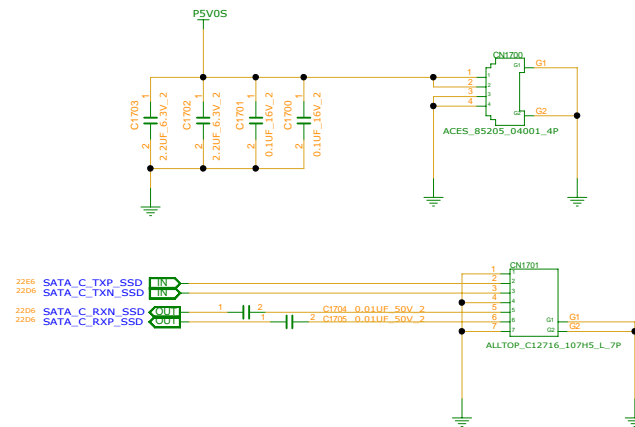


INVENTEC

Title		Block		Diagram	
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SHEET		1		of 04	

CHANGE by: *YefRemy* DATE: *24-APR-2012*

# SATA 7PIN CONN (FOR 2.5" SSD)

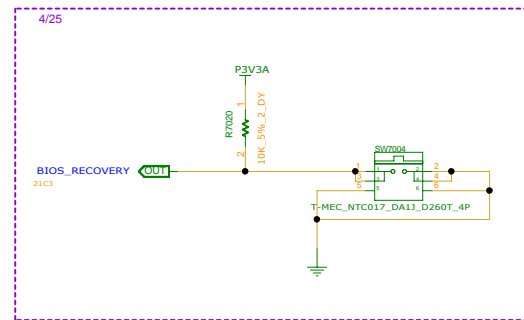
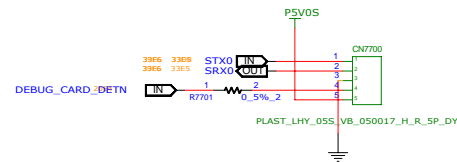


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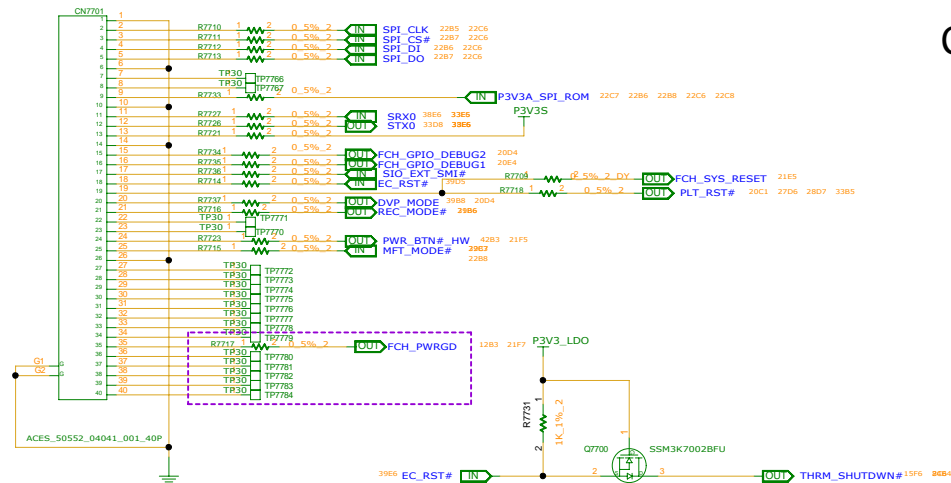
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CHANGE by: *YuFeng* DATE: 24-APR-2012

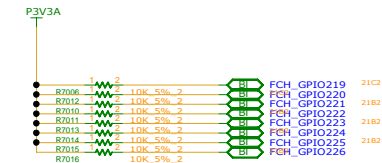
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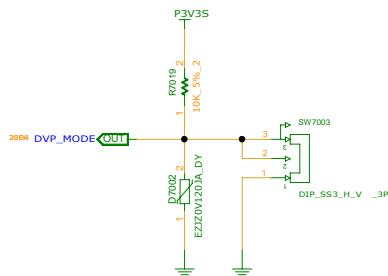
# GOOGLE DEBUG HEADER



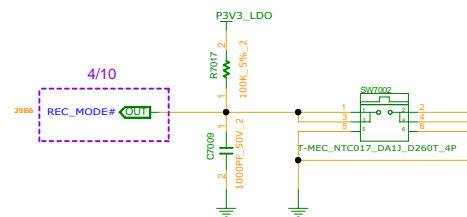
# CHROME OS DEBUG PORT



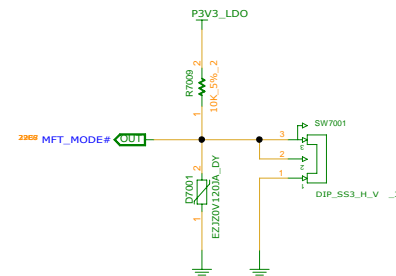
# DEVELOPER SWITCH



# RECOVERY BUTTON



# FW WRITE PROTECT SWITCH



INVENTEC

Title		Eris	
Block		Diagram	
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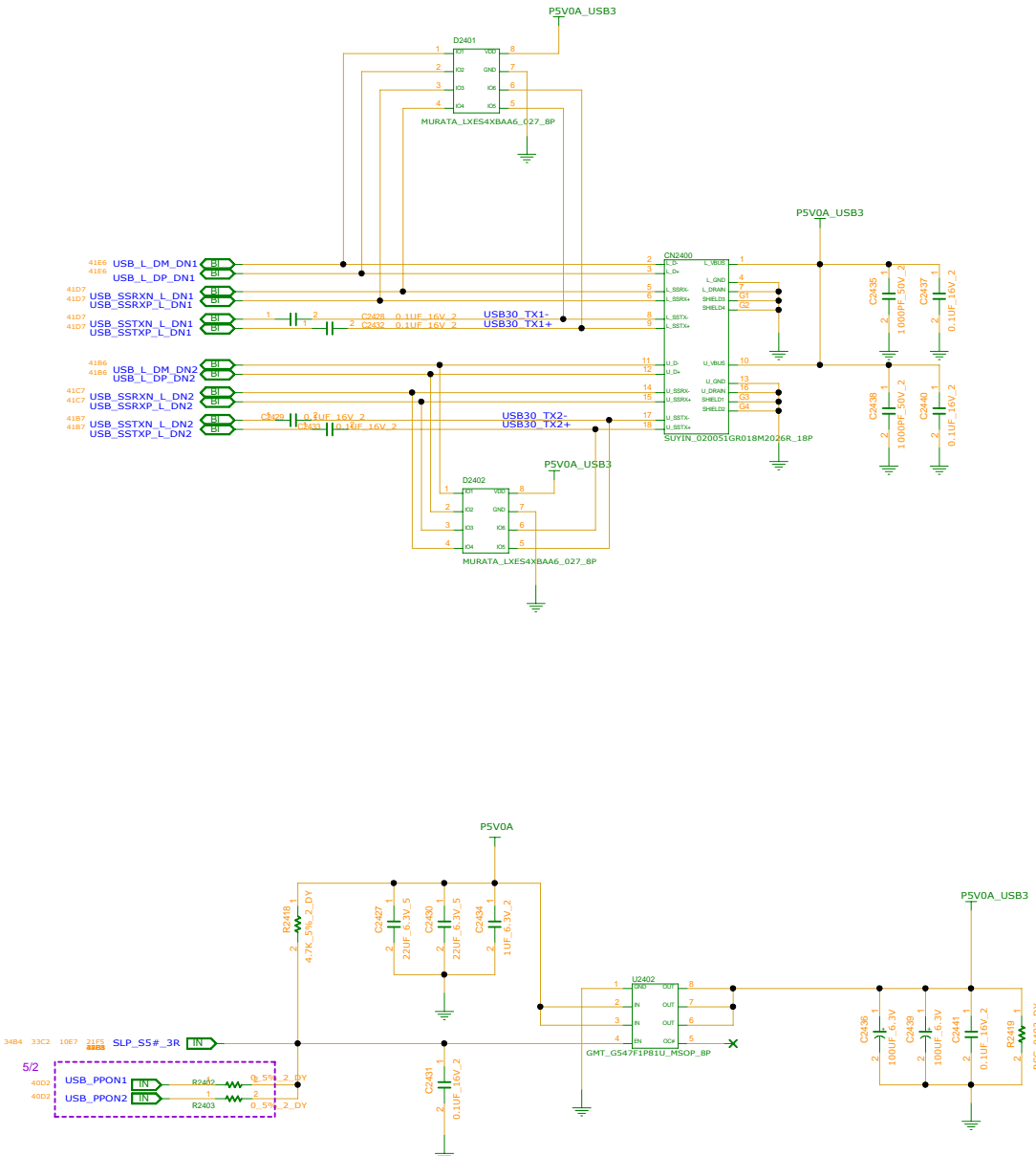
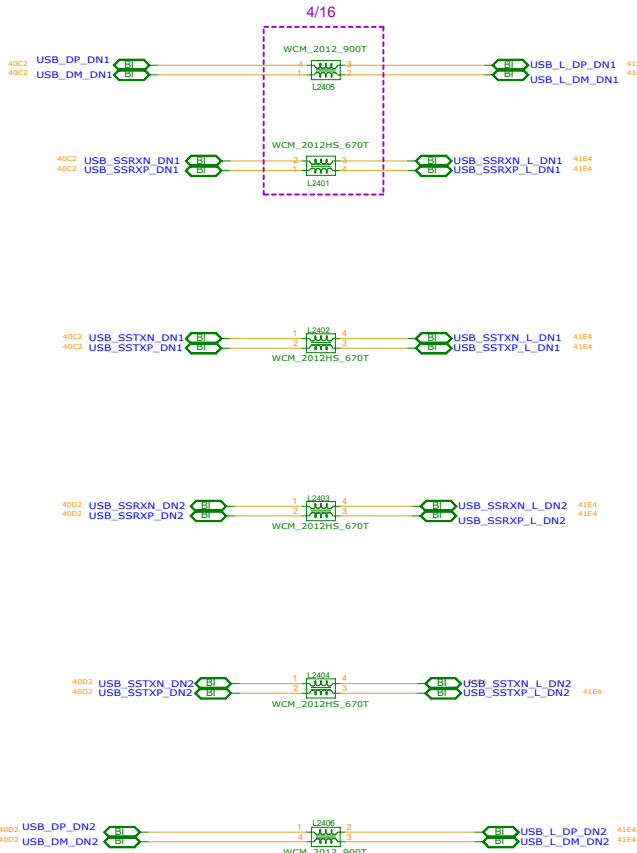
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doi:10.1017/S0022292412001609

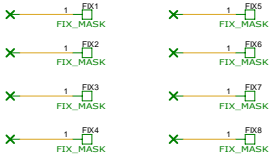
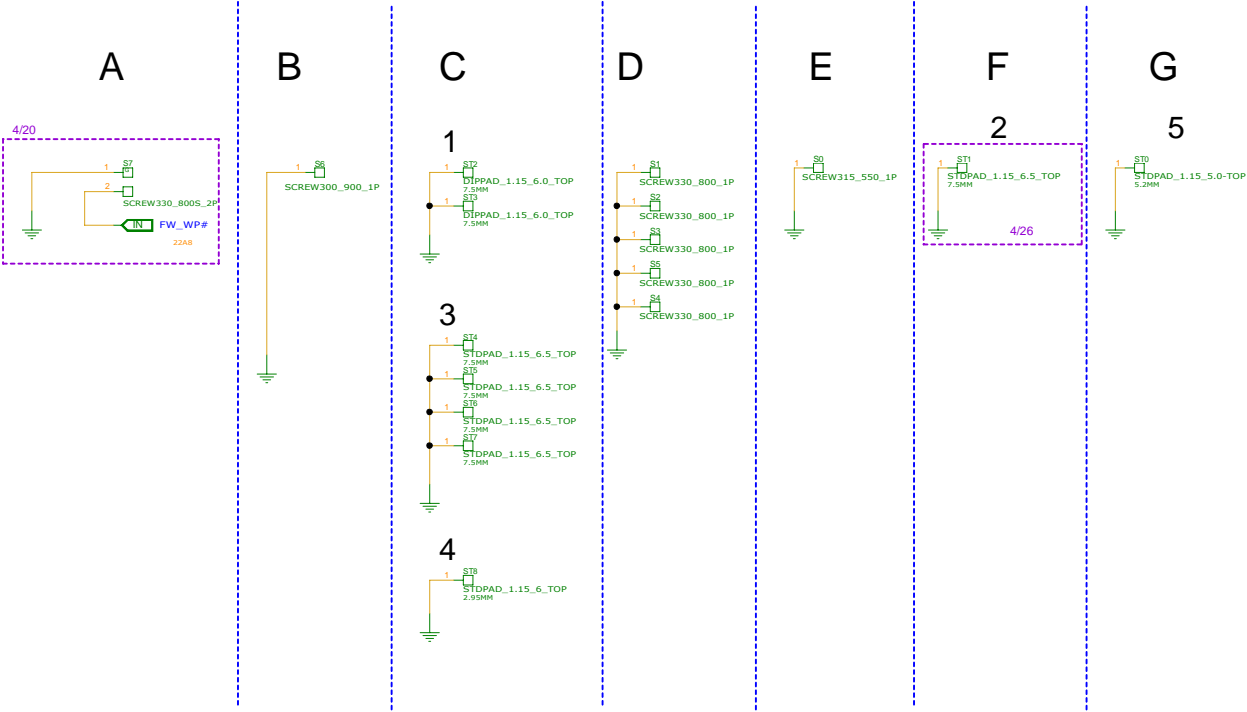


## DUAL USB3.0 CONN

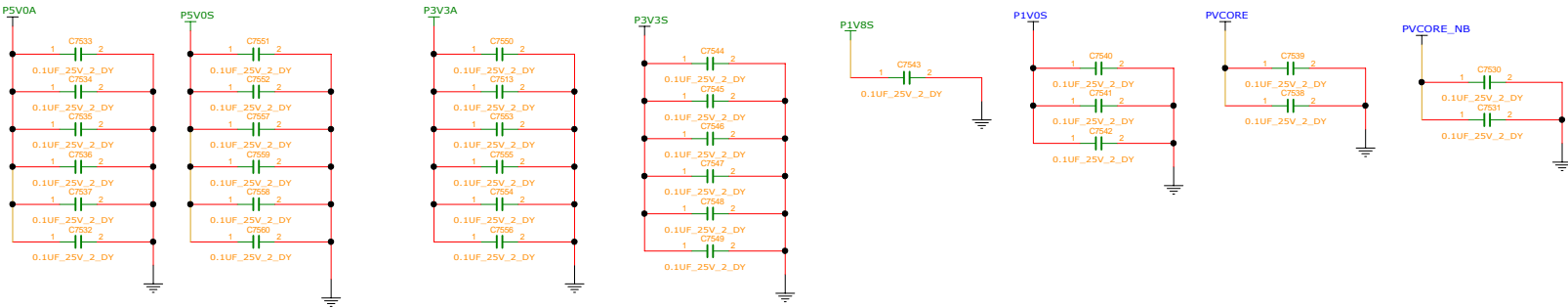




# SCREW



# EMI



INVENTEC			
Title			
Eris			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
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8 7 6 5 4 3 2 1  
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F HSF Property: ROHS or Halogen-Free

# ERIS AUDIO DB

2012.04.20

24-April-2012		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE Eris Everest Main Board			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER:	SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
FILE NAME								
PIN					SHEET 04 of 04			

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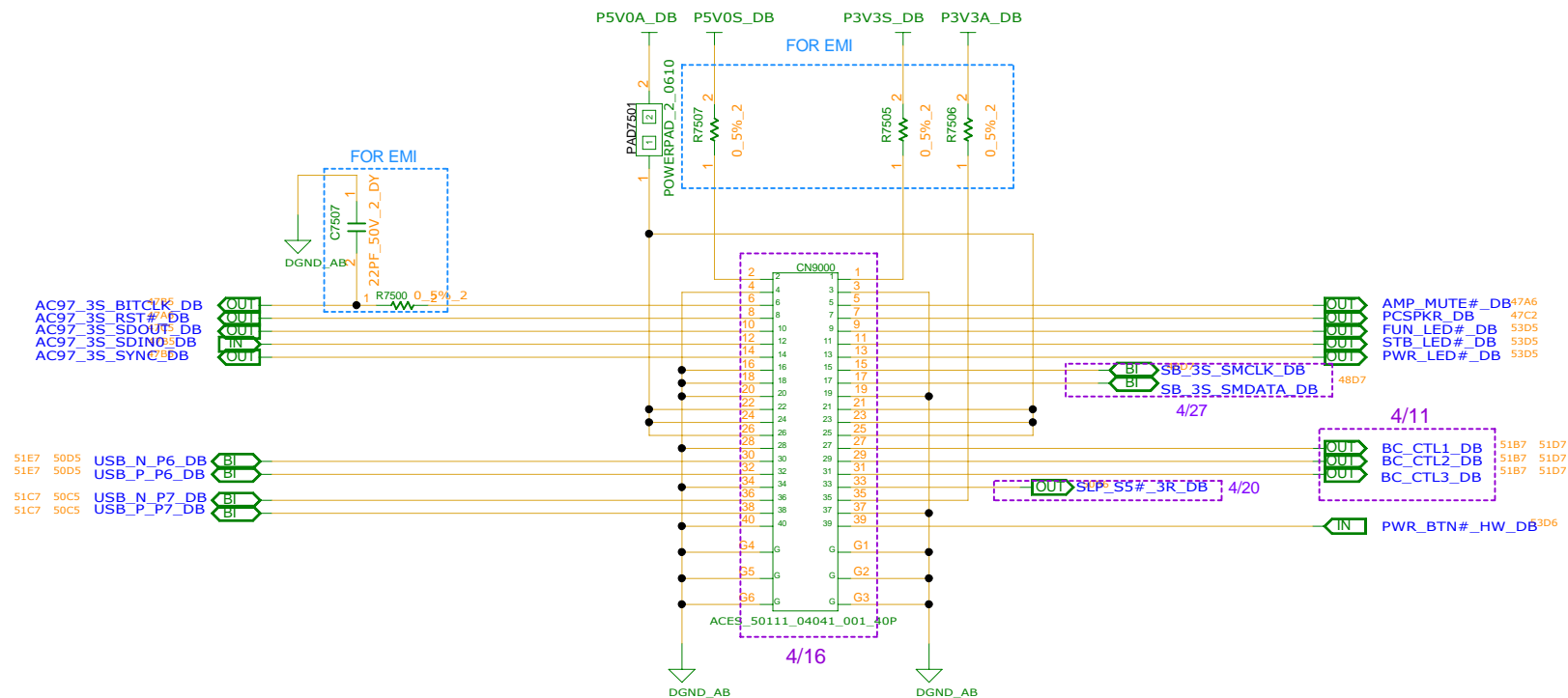
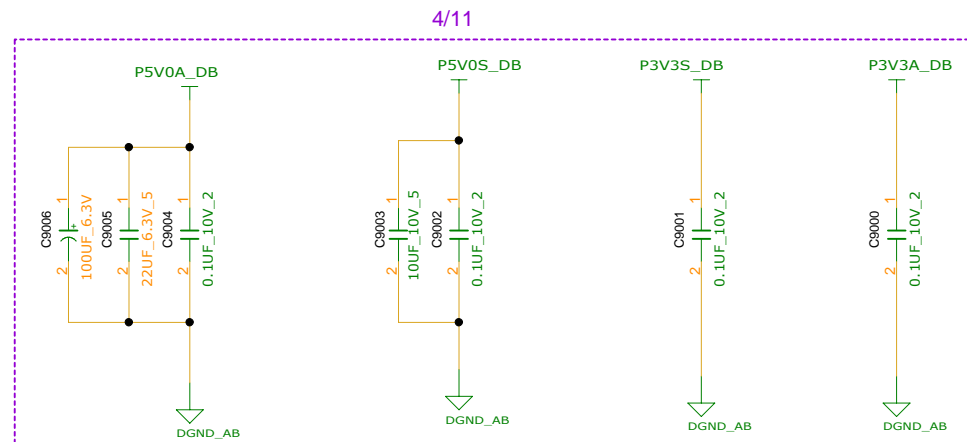
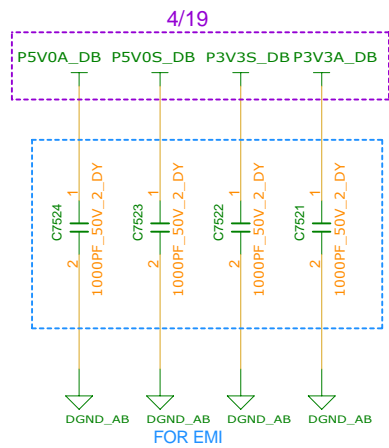
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INVENTEC

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Eris Block Diagram			
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CHANGE by: Jeffrey DATE: 24-Apr-2012

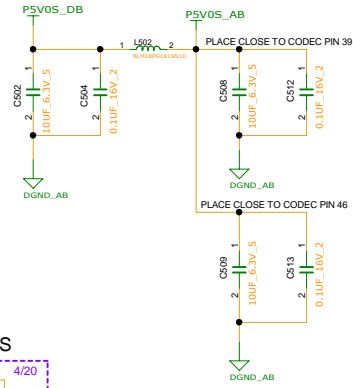
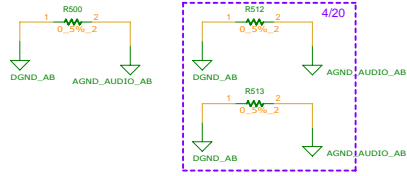


INVENTEC

TITLE			
Eriss Block Diagram			
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# CODEC

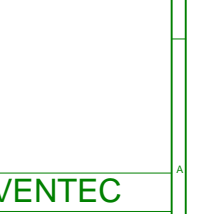
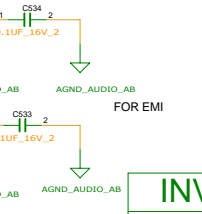
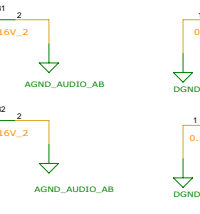
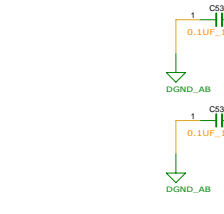
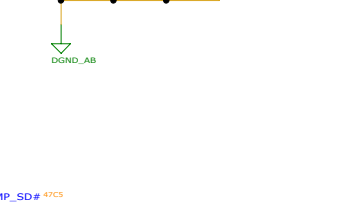
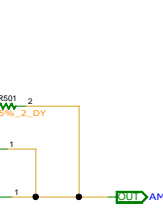
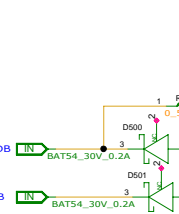
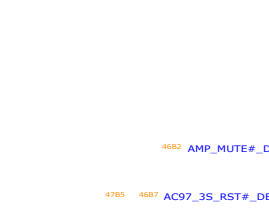
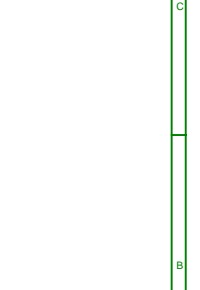
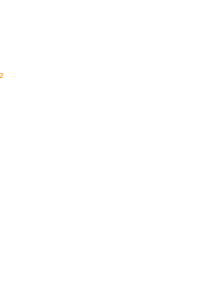
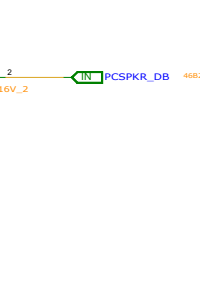
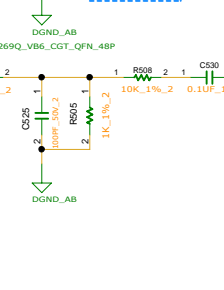
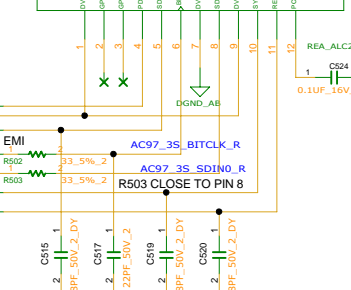
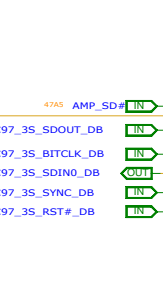
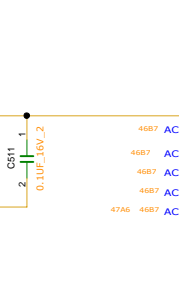
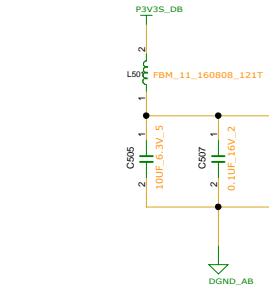
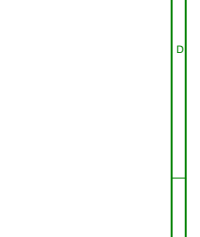
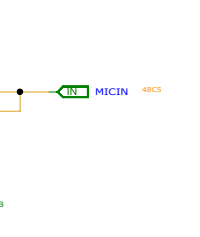
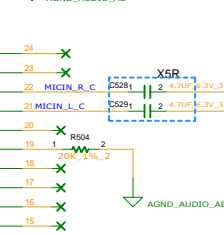
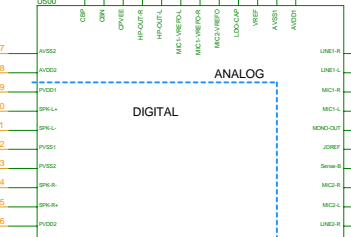
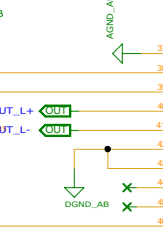
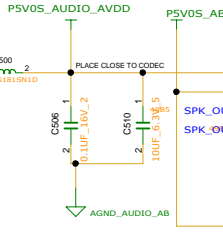
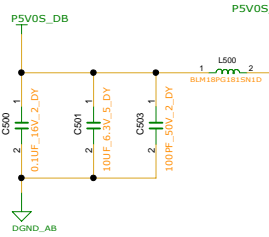
LOCATE UNDER CODEC  
USE 80 MILLS WIDE TRACE  
BRIDGING AGND AND DGND PLANES



48CS MICIN\_REF\_L

49D4 HPL

49D4 HPR

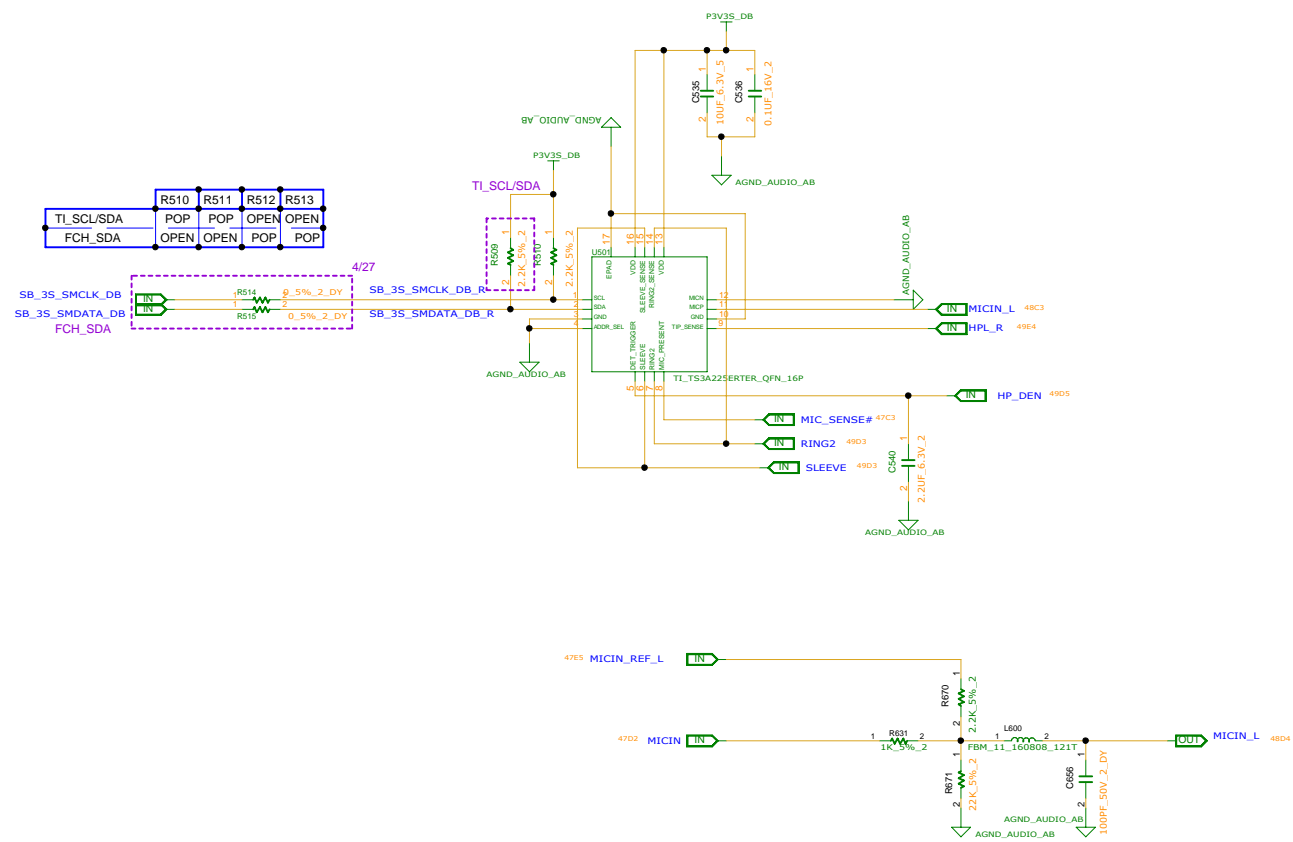


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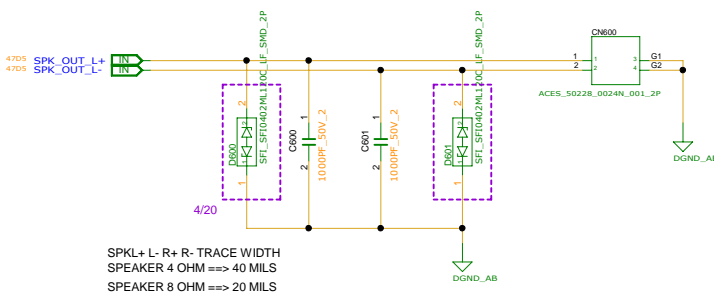
CHANGE by: YefMeyyev DATE: 24-APR-2012

HP DETECT





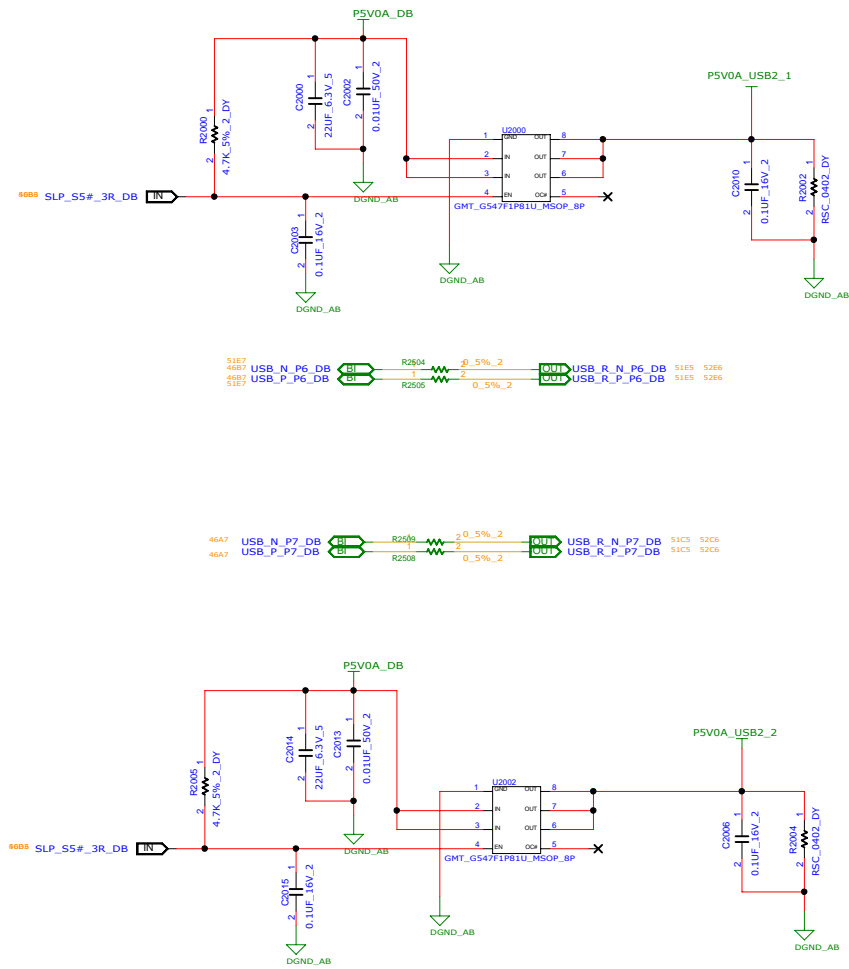
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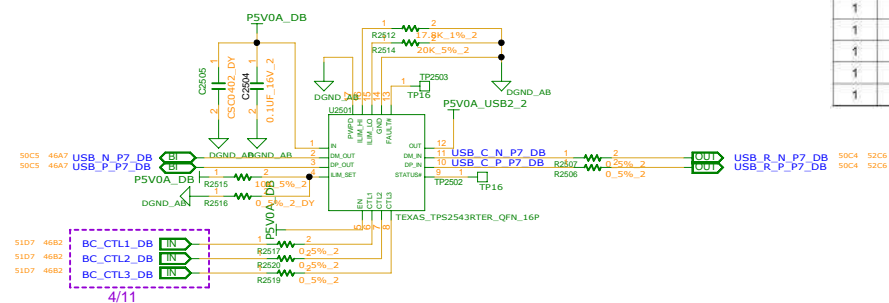
INVENTEC			
TITLE			
Eris Block Diagram			
SIZE C	CODE CS	DOC NUMBER 1310A25269-0-0	REV X01
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USB CONN FRONT SIDE

W/O BATTERY CHARGE

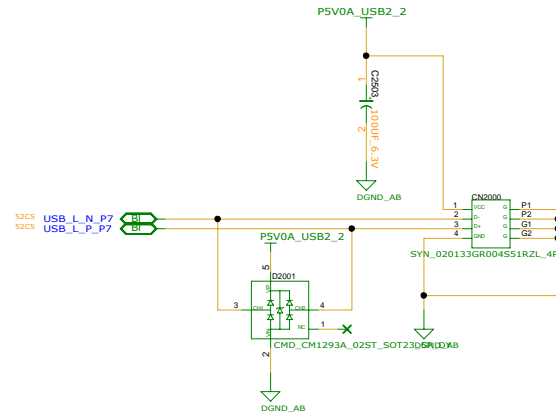
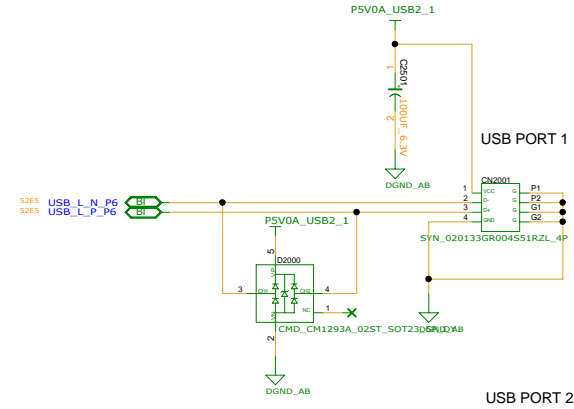
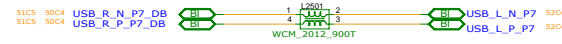
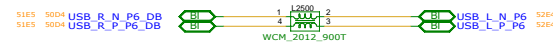


## W/ BATTERY CHARGE



CTL1	CTL2	CTL3	ILIM_SEL	MODE	CURRENT LIMIT SETTING	STATUS OUTPUT (Active low)	COMMENT
0	0	0	0	Discharge	NA	OFF	OUT held low
0	0	0	1	Discharge	NA	OFF	
0	0	1	0	DCP_Auto	ILIM_HI	OFF	
0	0	1	1	DCP_Auto	$I_{OS\_PW}$ & ILIM_HI <sup>(1)</sup>	DCP load present <sup>(2)</sup>	Data Lines Disconnected and Load Detect Function Active
0	1	0	0	SDP	ILIM_LO	OFF	Data Lines connected
0	1	0	1	SDP	ILIM_HI	OFF	
0	1	1	0	DCP_Auto	ILIM_HI	OFF	Data Lines Disconnected
0	1	1	1	DCP_Auto	ILIM_HI	DCP load present <sup>(3)</sup>	Data Lines Disconnected and Load Detect Function Active
1	0	0	0	DCP_Shorted	ILIM_LO	OFF	Device Forced to stay in DCP BC 1.2 charging mode
1	0	0	1	DCP_Shorted	ILIM_HI	OFF	
1	0	1	0	DCP / Divider1	ILIM_LO	OFF	
1	0	1	1	DCP / Divider1	ILIM_HI	OFF	Device Forced to stay in DCP Divider: 1 Charging Mode
1	1	0	0	SDP	ILIM_LO	OFF	
1	1	0	1	SDP	ILIM_HI	OFF	
1	1	1	0	SDP <sup>(4)</sup>	ILIM_LO	OFF	Data Lines Connected
1	1	1	1	CDP <sup>(4)</sup>	ILIM_HI	DCP load present <sup>(5)</sup>	
							Data Lines Connected and Load Detect Active

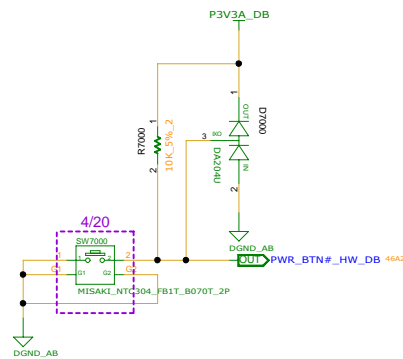
# USB CONN FRONT SIDE



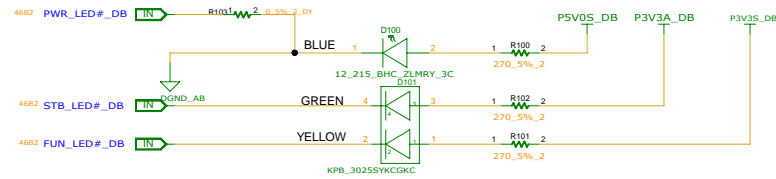
INVENTEC

Title		Eris	
Block		Diagram	
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A25269-0-0	X01
SHEET		02	of 04

POWER SWITCH



LED



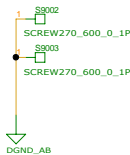
PWR_LED#	BLUE
STB_LED#	GREEN
FUN_LED#	YELLOW

INVENTEC

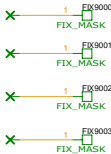
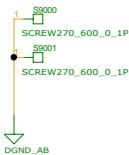
Title		Eris		Block Diagram	
SIZE	C	CODE	CS	DOC NUMBER	1310A25269-0-0
SHEET		01	of	04	REV 201

# SCREW

H



I



# EMI

INVENTEC

Title			
Eris Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A25259-0-0	X01
SHEET		of	of